

# Thermal Analysis Simulation about mini Port Card with ANSYS

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## Parameters

Temperature ( degree C )

- Air = 5
- Coolant = -15

Heat transfer coefficient : h (W/m<sup>2</sup> k)

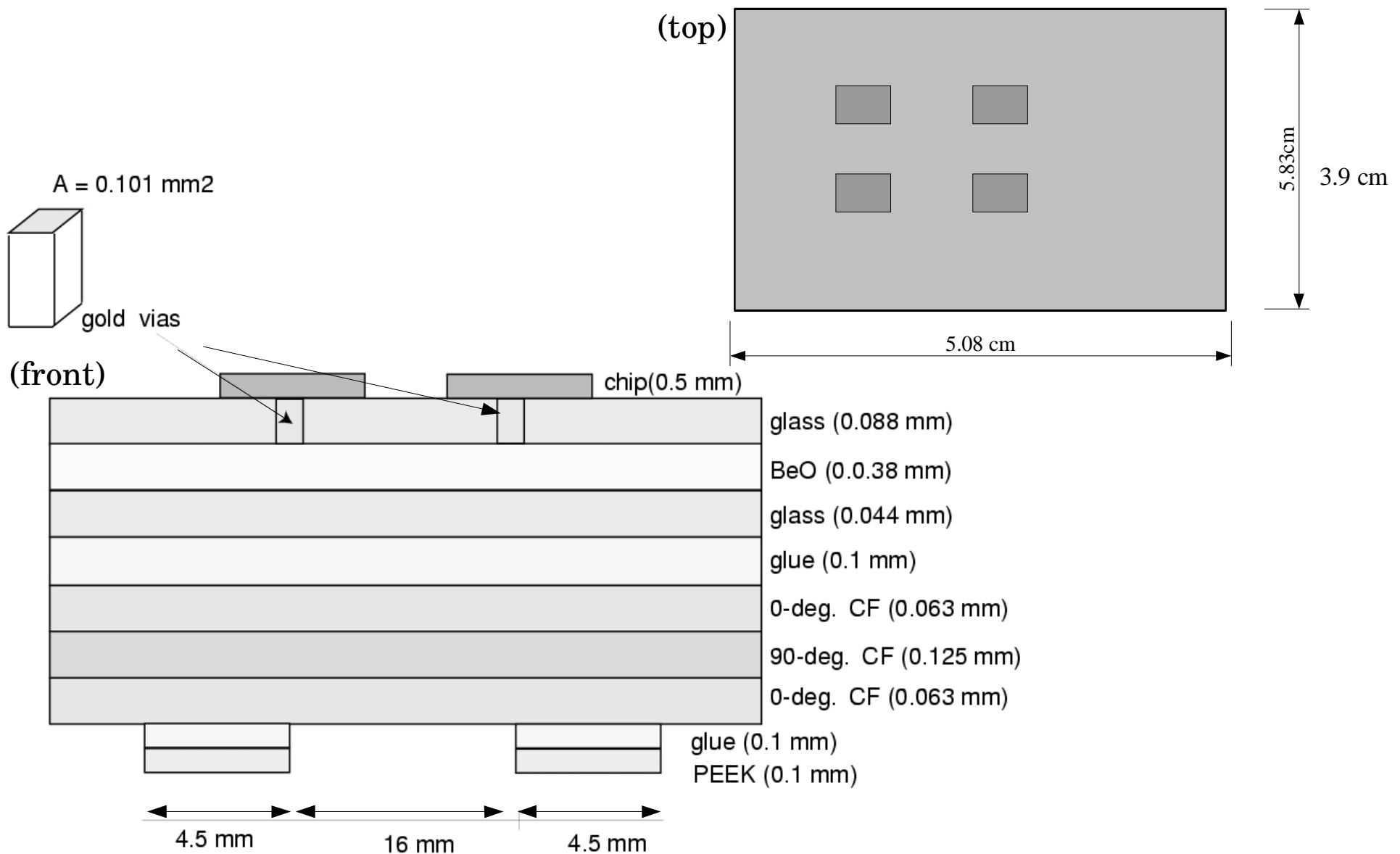
- Air = 3
- Coolant = 700

Thermal conductivity : k (W/m k)

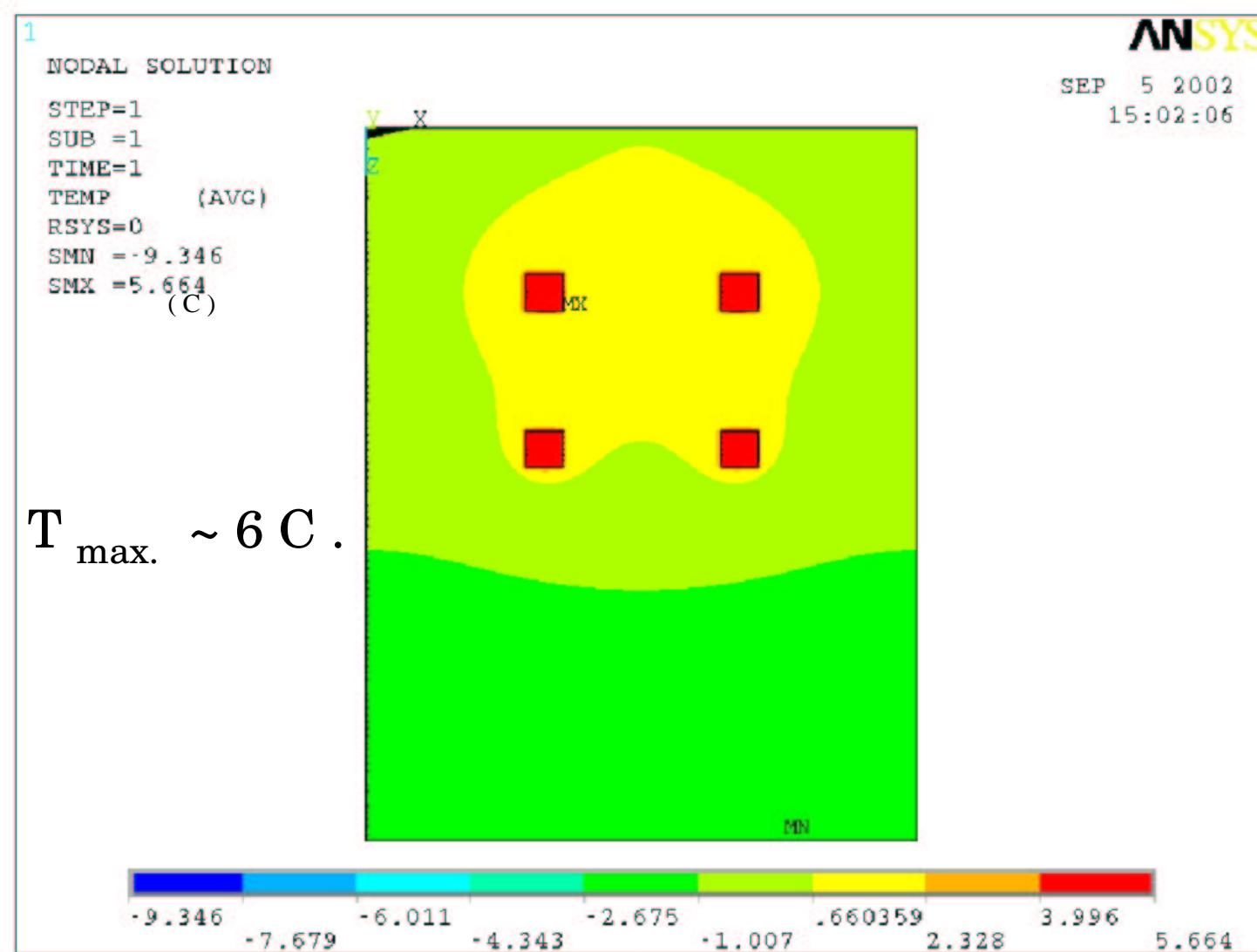
- Silicon(chip) = 148
- Glass = 1.0
- Gold = 317
- Copper = 401
- BeO = 248
- Glue = 0.1
- Carbon Fiber = 372(0 deg.) / 1(90 deg.)
- PEEK = 0.25
- Polyimide = 0.12

Heat rate  
0.5 W/chip

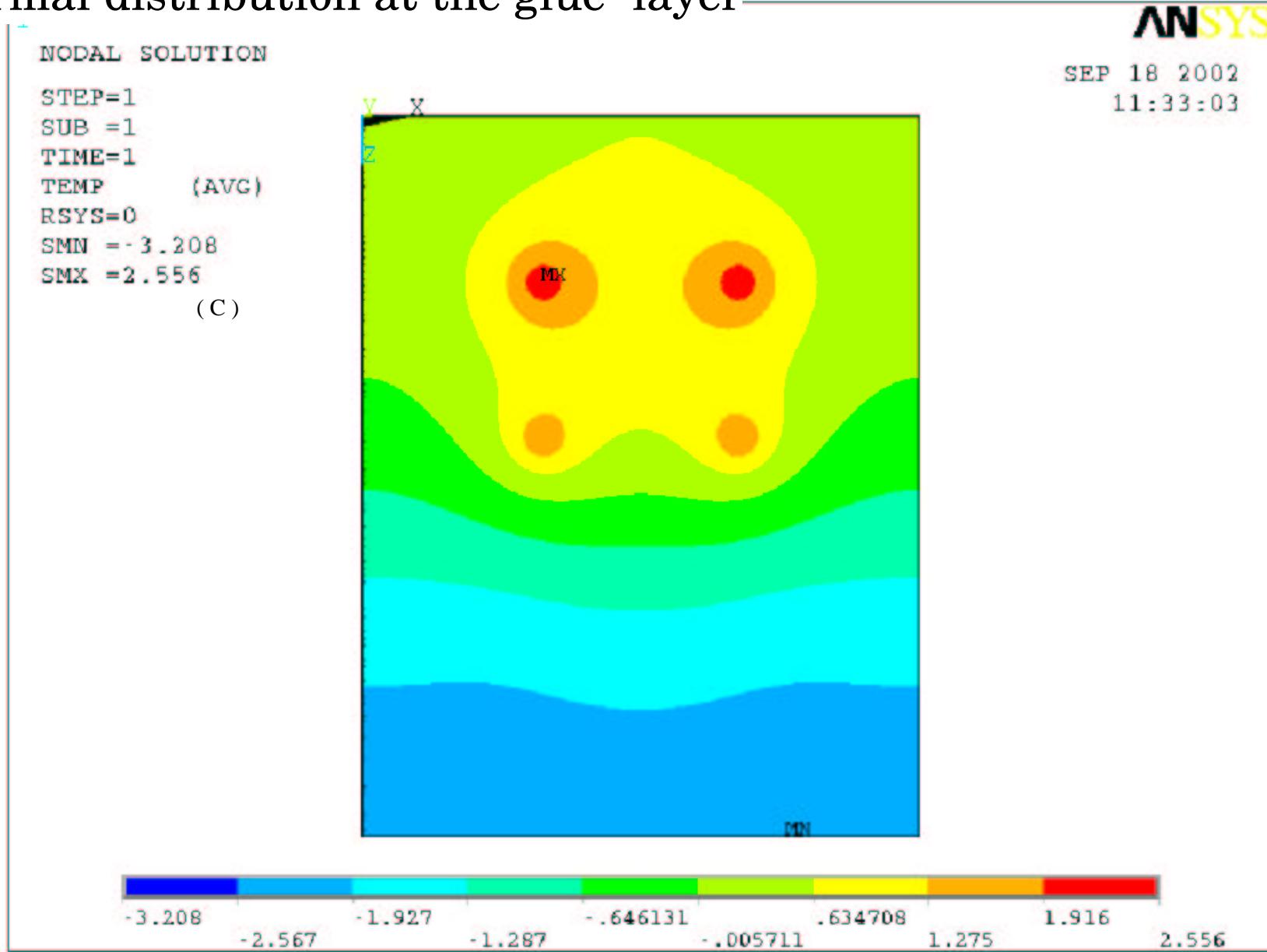
# Geometry of BeO miniPC used in this simulation



# Thermal distribution of BeO miniPC



# Thermal distribution at the glue layer



## Modifying the thermal conductivity of gold vias

Measured thermal conductivity is usually less than the pure metal conductivity.

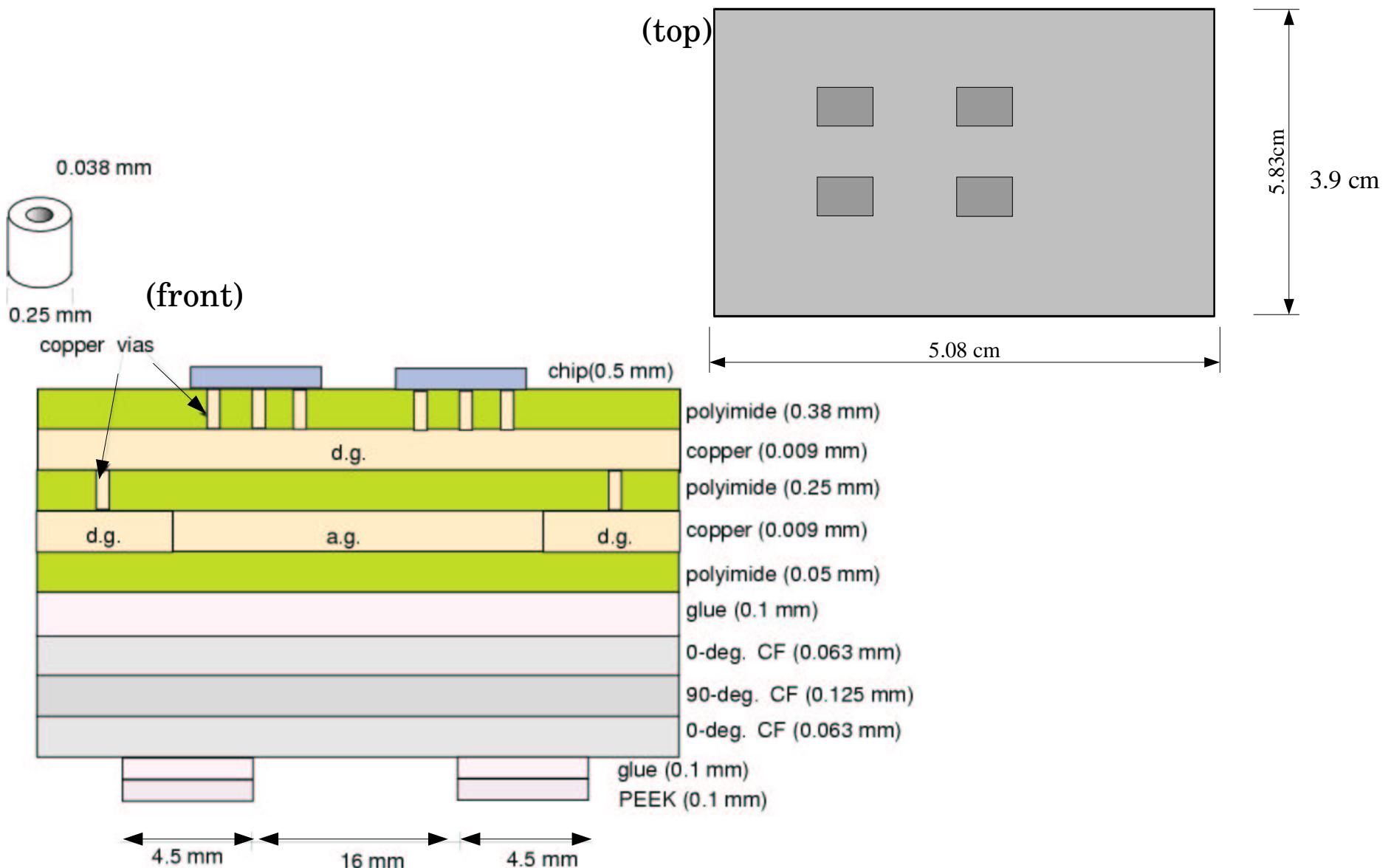
$$\begin{aligned}\text{Modified } k_{\text{gold}} &= (k_{\text{gold\_pure}}) * (275 / k_{\text{silver\_pure}}) \\ &= 203 \text{ (W/m k)}\end{aligned}$$

### Results

	k_gold (W/m k)	Max. Temp.(C)
Pure	317	5.66
Modified	203	5.83

This effect is small.

# Geometry of polyimide miniPC used in this simulation



## Maximum temperature of polyimide miniPC, for different number of vias .

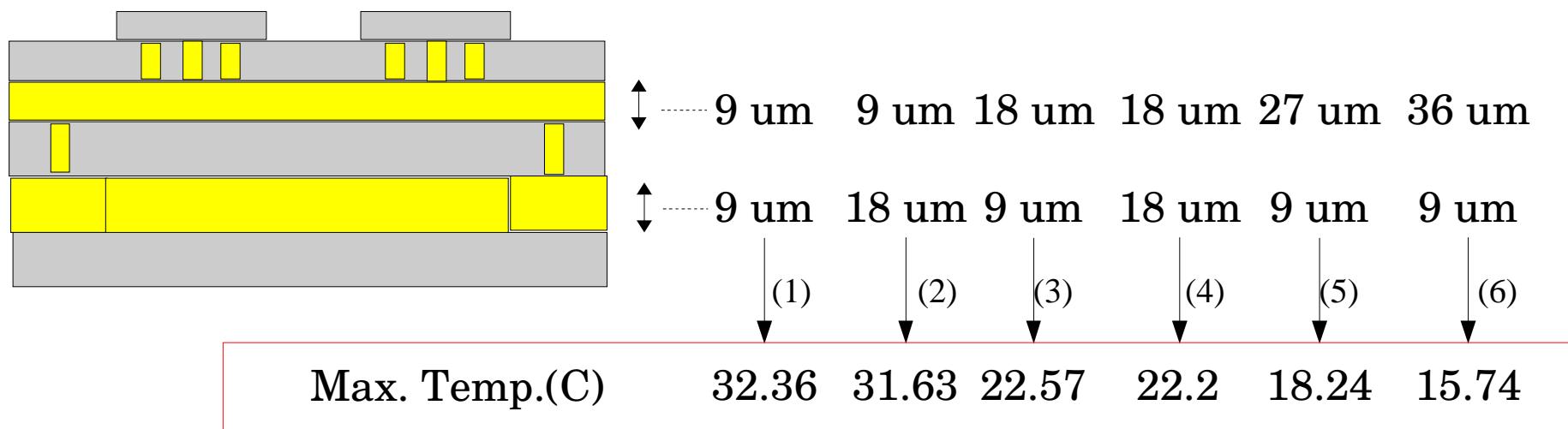
# of vias

Under each chips	Between the copper GNDs	Max.Temp.(C)
4	10	40.59
4	20	40.15
9	10	32.41
9	20	32.36

The number of vias under the chips contributes the temperature,  
but between the GNDs doesn't.

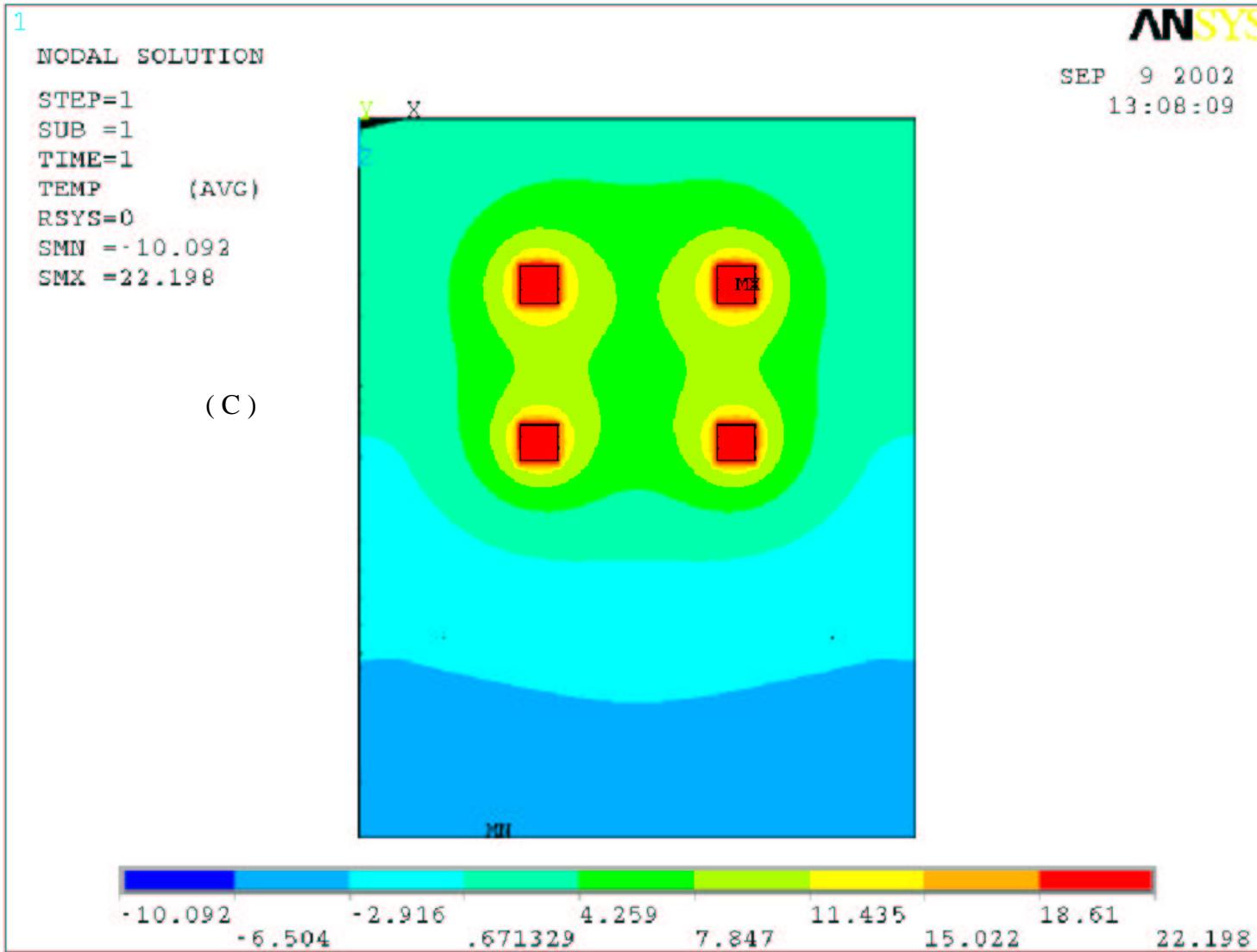
# Changing thickness of the copper GNDs

(for polyimide MPC with 9 vias under the chips and 20 between the GNDs. )



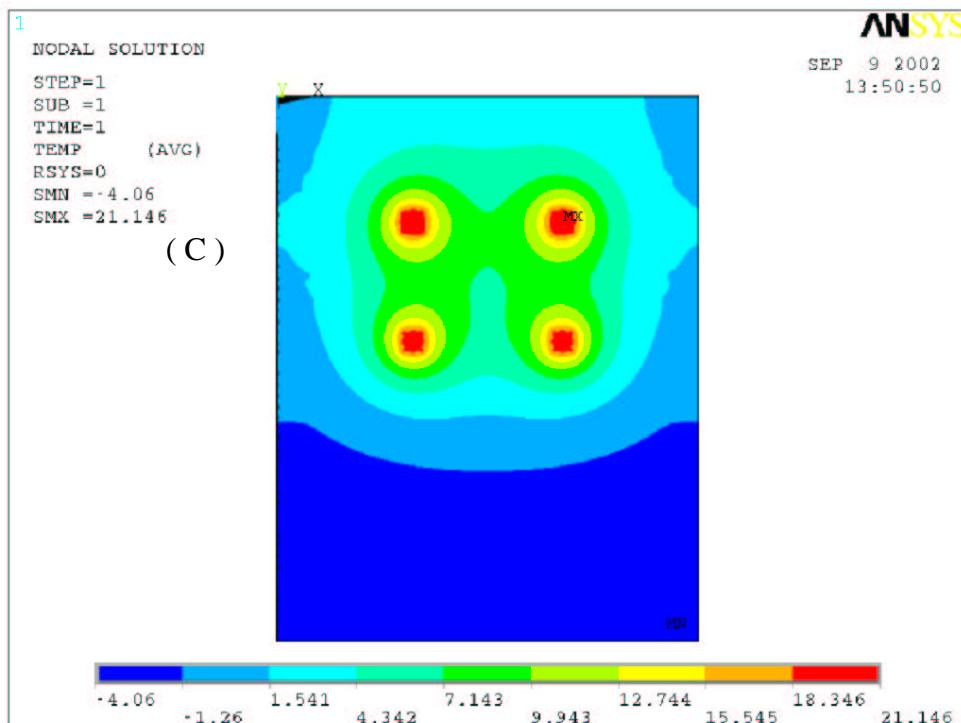
Up-side GND helps cooling down,  
but down-side doesn't .

## Thermal distribution for the case (4)

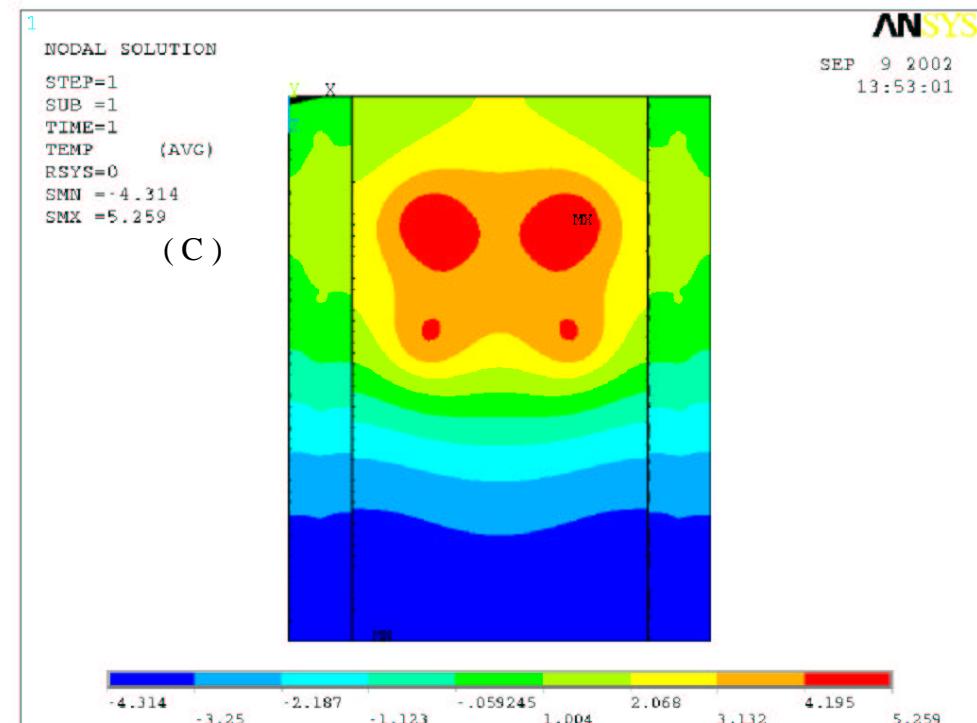


## Thermal distribution at the GND layers for the case (4)

up-side



down-side



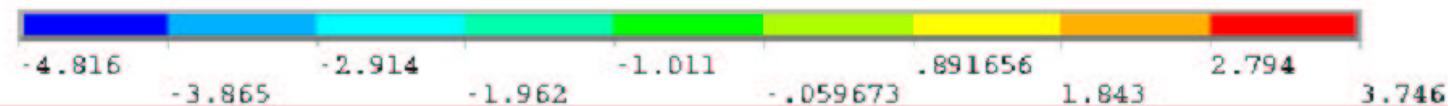
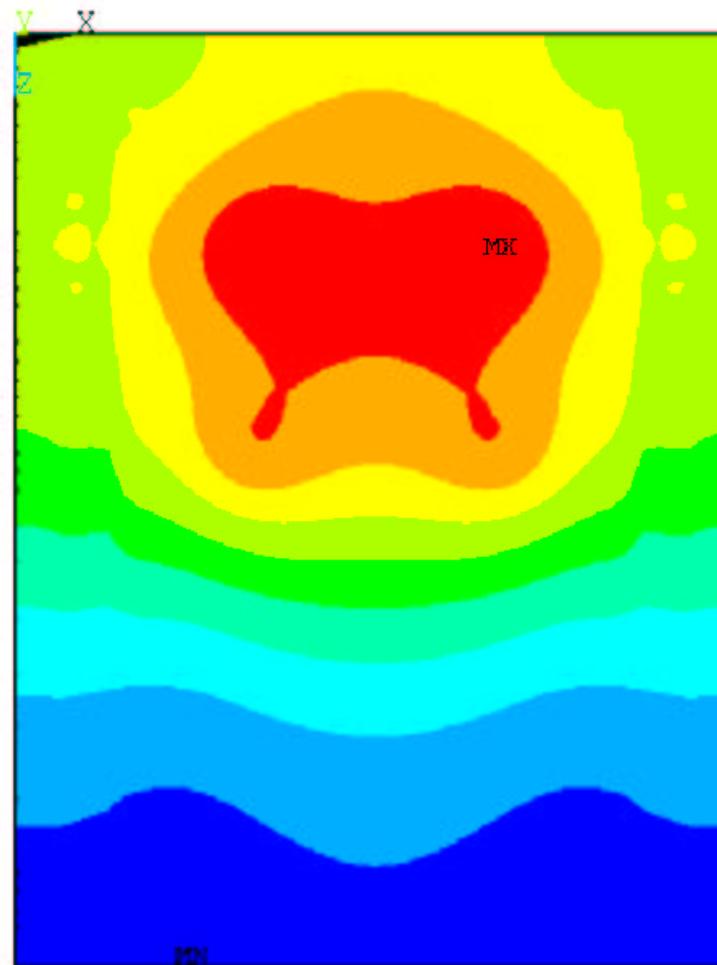
# Thermal distribution at the glue for the case (4)

ANSYS

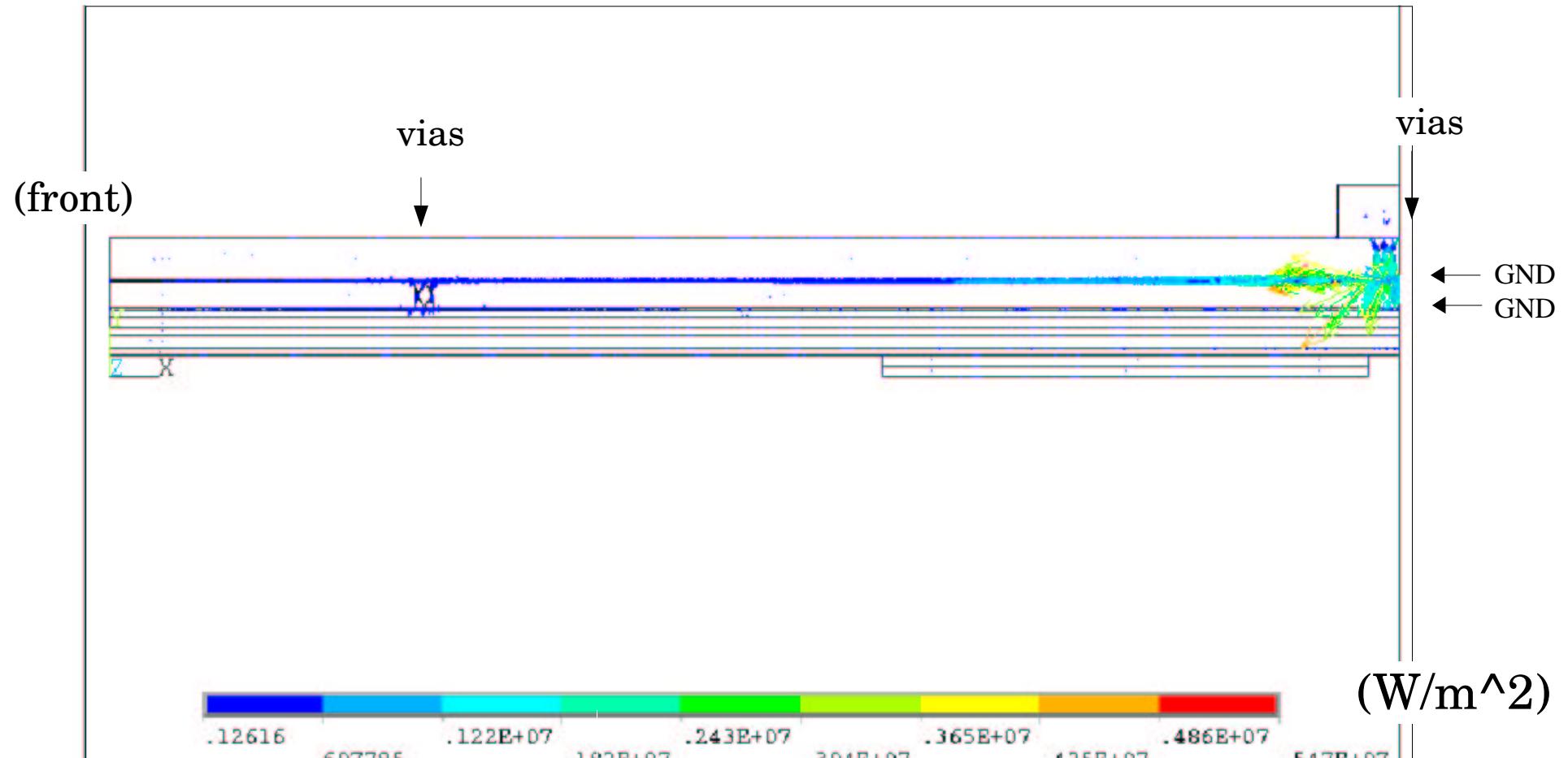
SEP 18 2002  
14:39:32  
ANSYS

## NODAL SOLUTION

STEP=1  
SUB =1  
TIME=1  
TEMP (AVG)  
RSYS=0  
SMN = -4.816  
SMX = 3.746 (C)

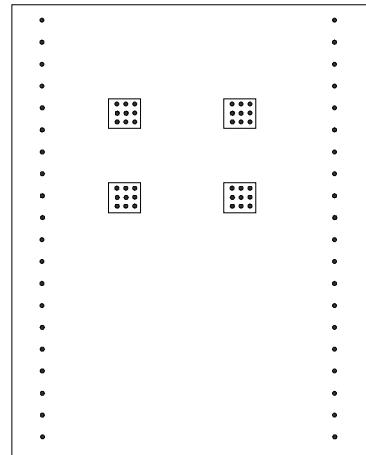


## Thermal Flux for the case (4)

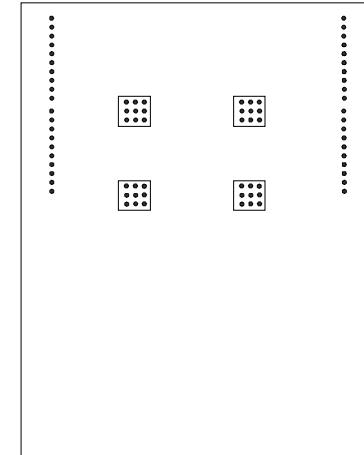


Heat flows mainly through the vias under the chips and then spreads out through the up-side copper GND.

## Comparing the temperature of miniPC which have different vias positions (for case (6))



A

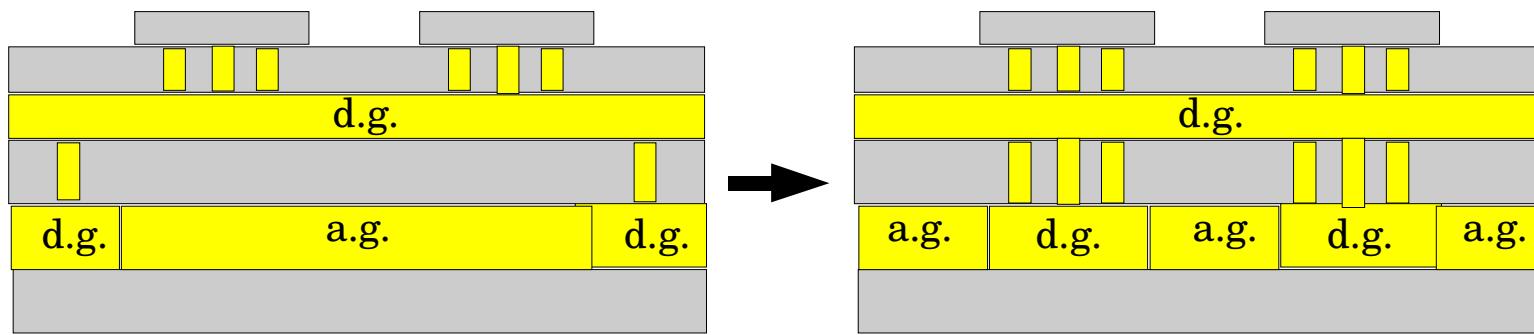


B

	Max. Temp. ( C )
A	15.735
B	15.726

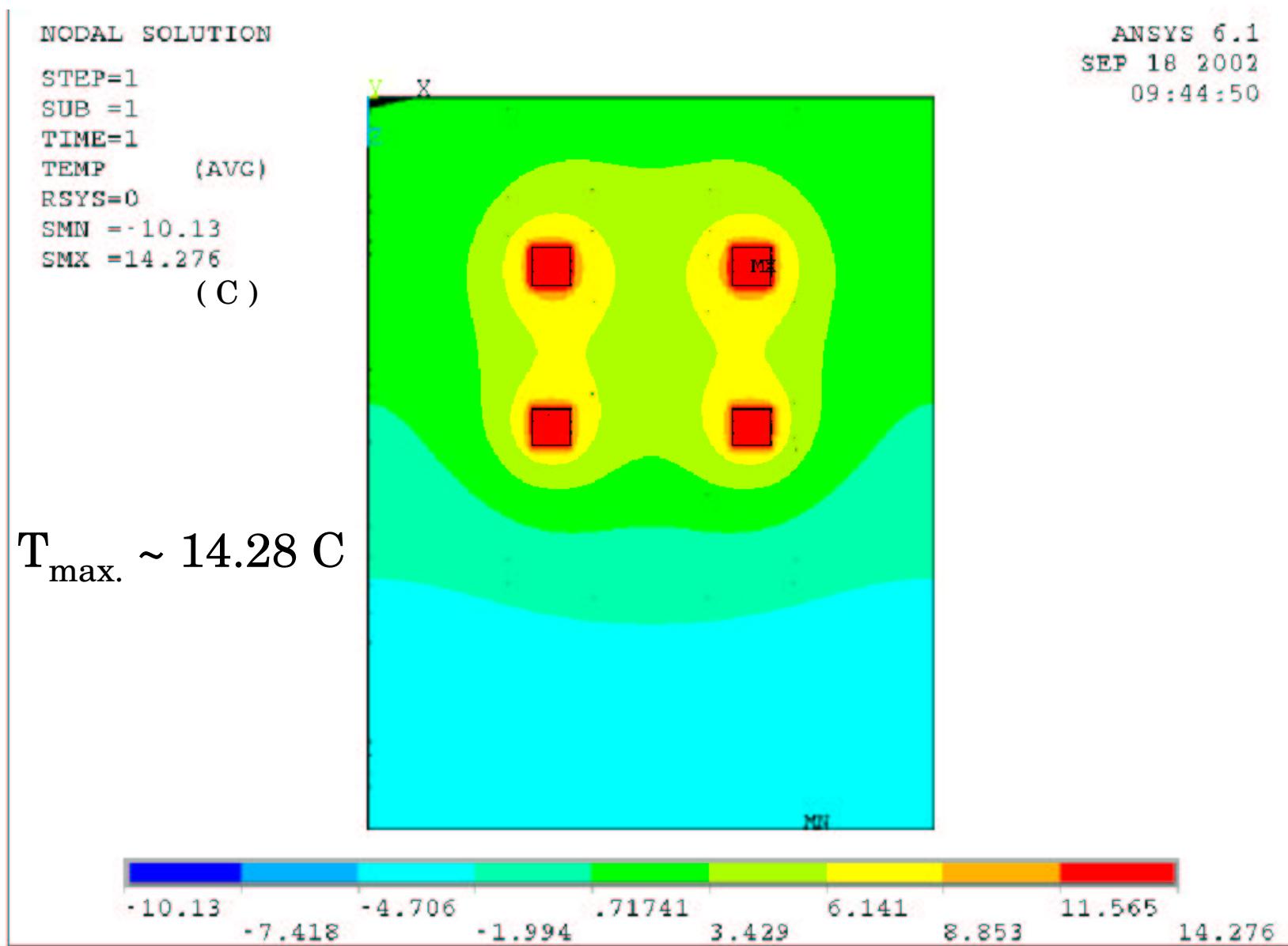
Positions of the vias between the GNDs give little contribution to the temperature .

Extending the vias to the "down-side" ground



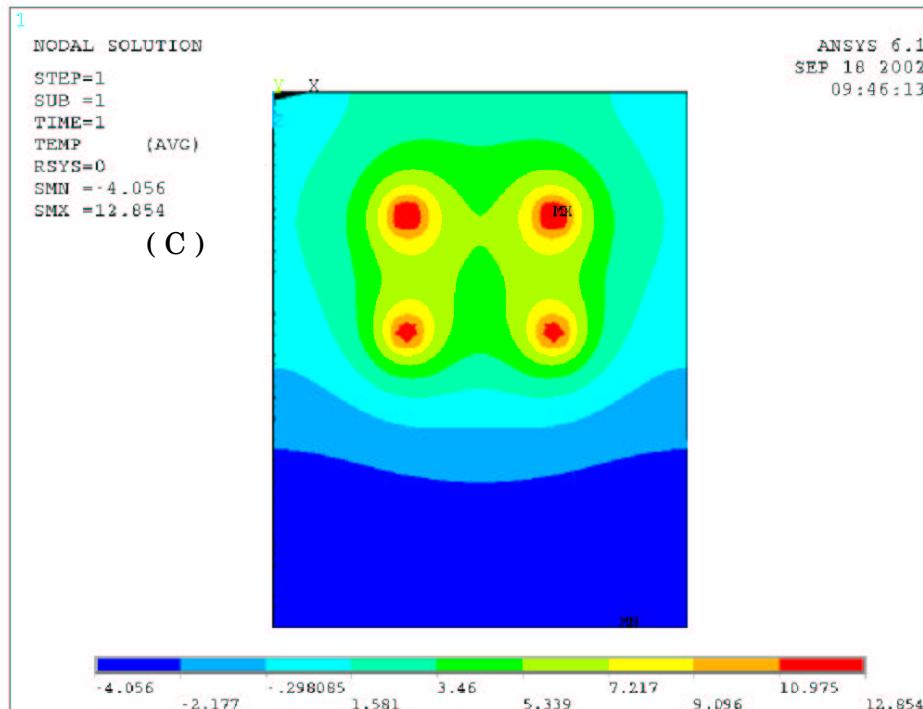
Not using the side vias, extend the vias under the chips  
to the "down-side" ground and move the down-side  
digital ground there.      (thickness of GND = 18  $\mu$ m)

# Thermal distribution for 9 vias MPC

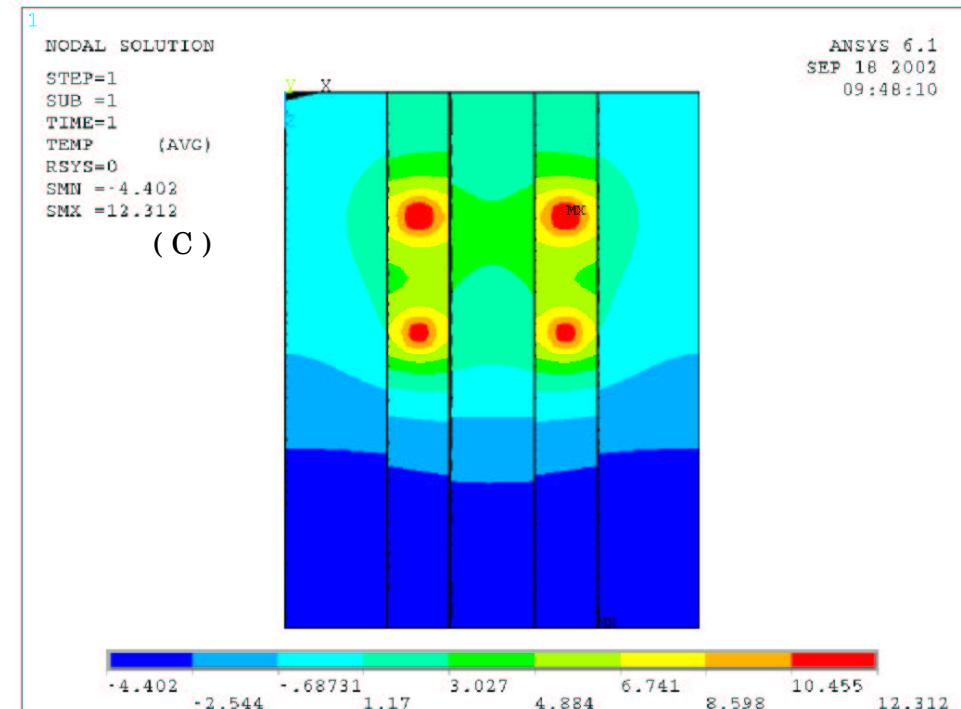


# Thermal distribution at the GND layers for 9 vias MPC

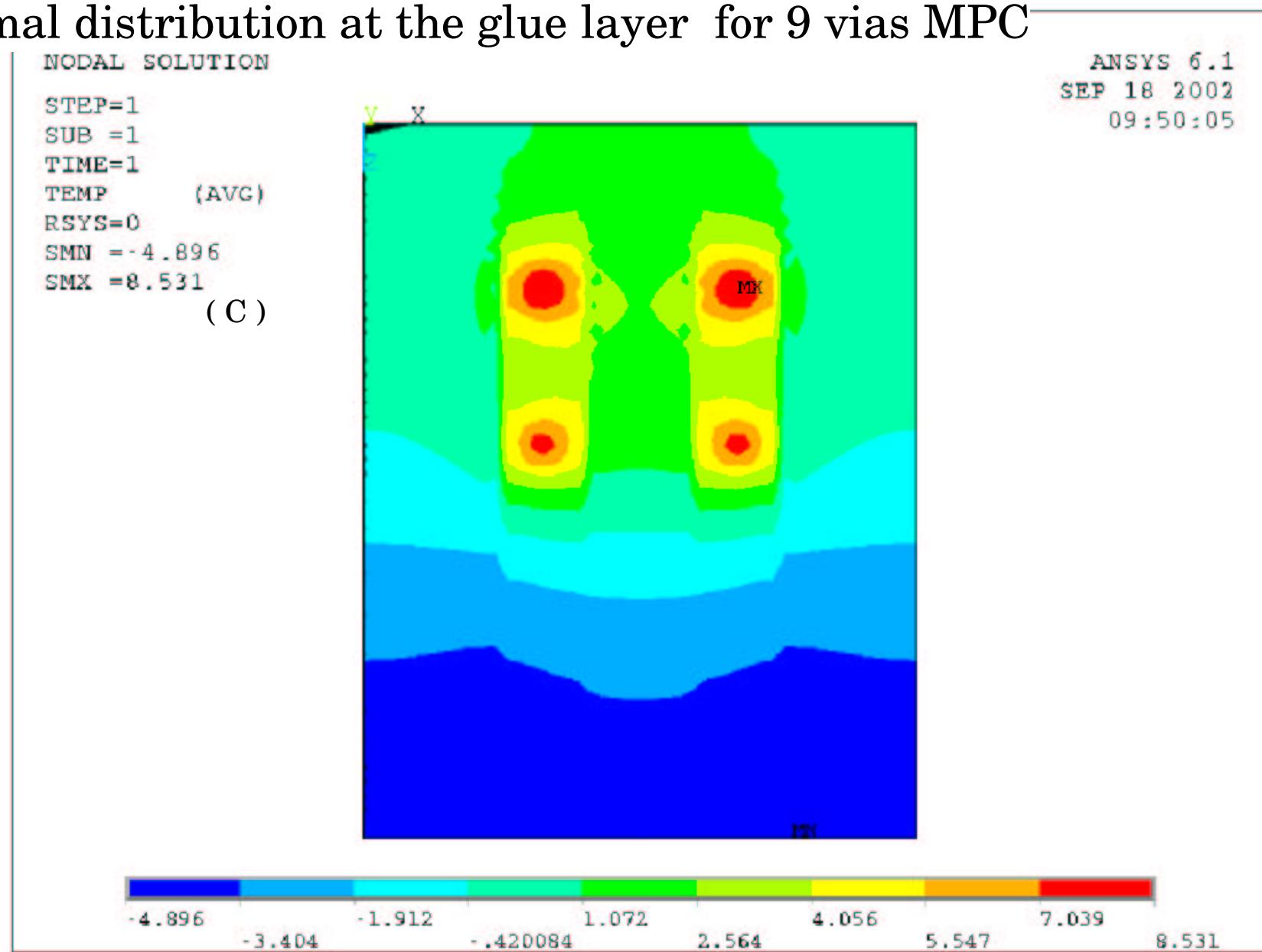
up-side



down-side



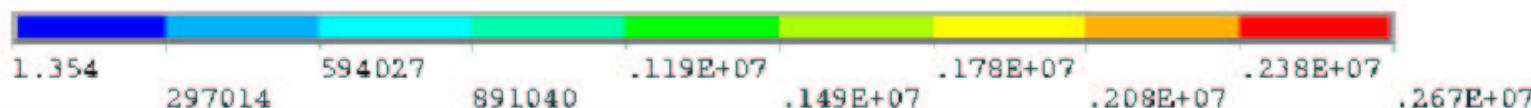
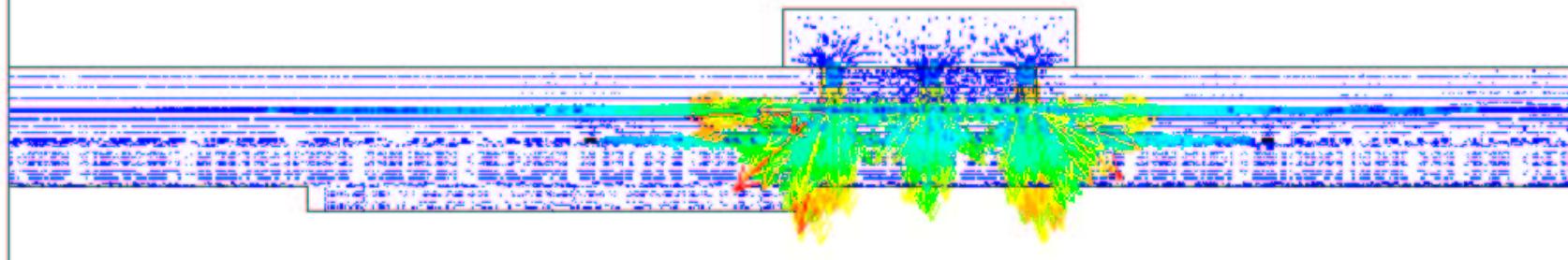
# Thermal distribution at the glue layer for 9 vias MPC



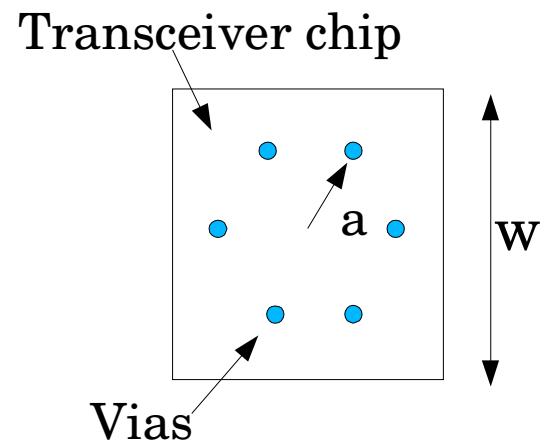
# Thermal flux for 9 vias MPC

```
VECTOR  
STEP=1  
SUB =1  
TIME=1  
TF  
ELEM=192397  
MIN=1.354  
MAX=.267E+07  
(W/m^2)
```

ANSYS 6.1  
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09:28:01



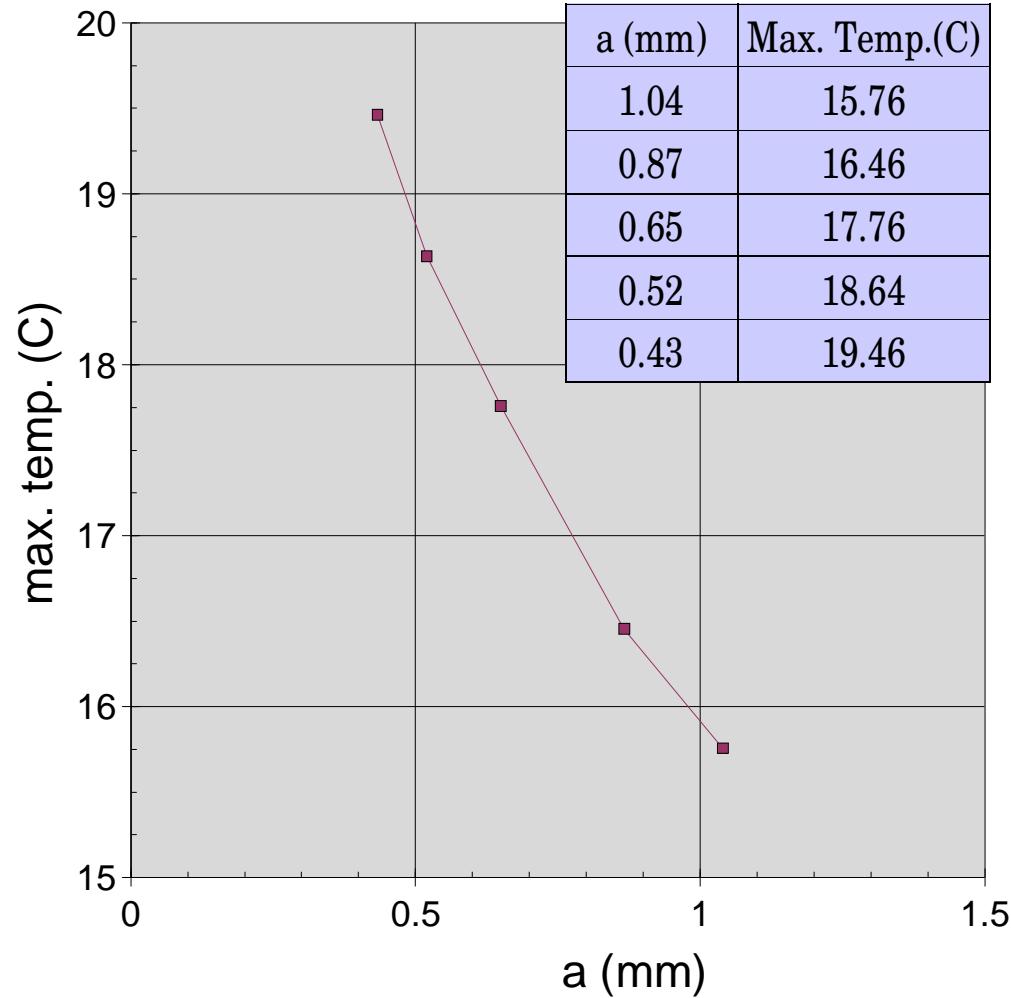
6 vias MPC (Change the number of vias (9 -> 6).)



Positions of the vias

$$\begin{aligned} a &= w/2.5, w/3, w/4, w/5, w/6 \\ &= 1.04, 0.87, 0.65, 0.52, 0.43 \text{ (mm)} \end{aligned}$$

Max. temp. ( C )



Wide distance between  
the vias is better.

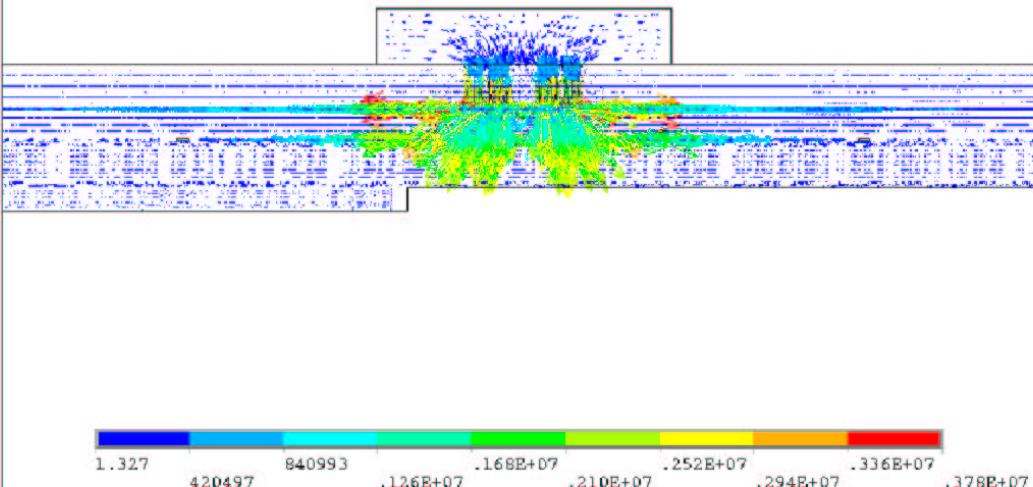
Thermal flux for 6 vias MPC

$a = 0.43 \text{ mm}$

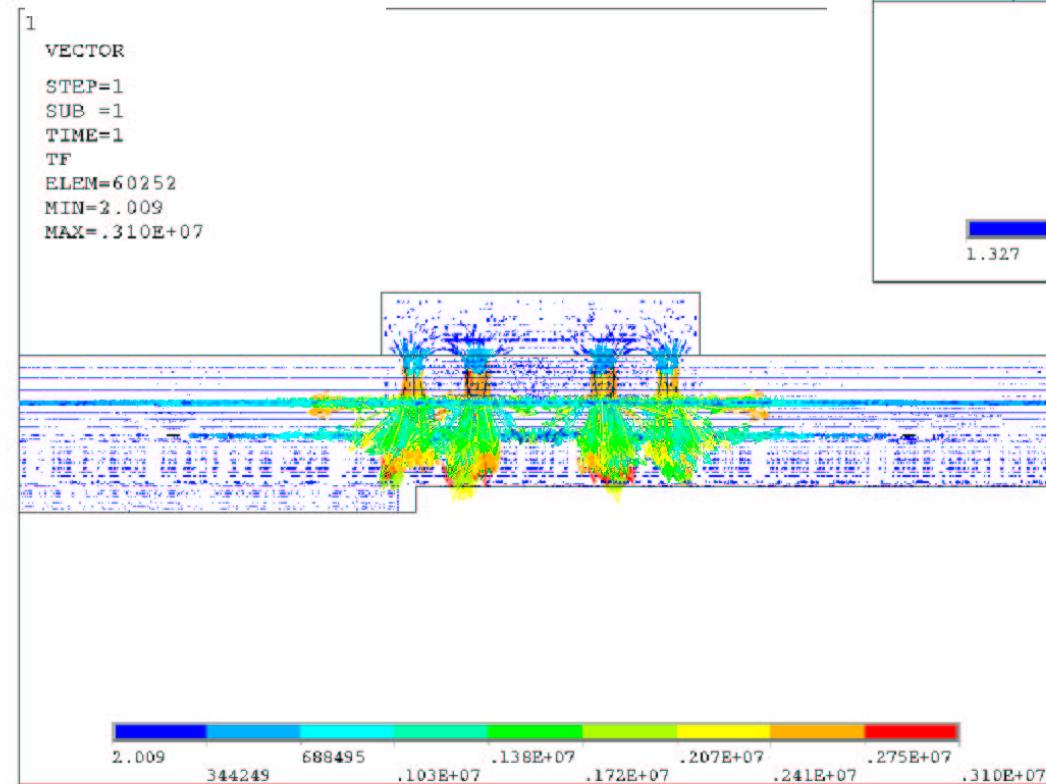
ANSYS

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13:42:36

VECTOR  
STEP=1  
SUB =1  
TIME=1  
TF  
ELEM=246523  
MIN=1.327  
MAX=.378E+07

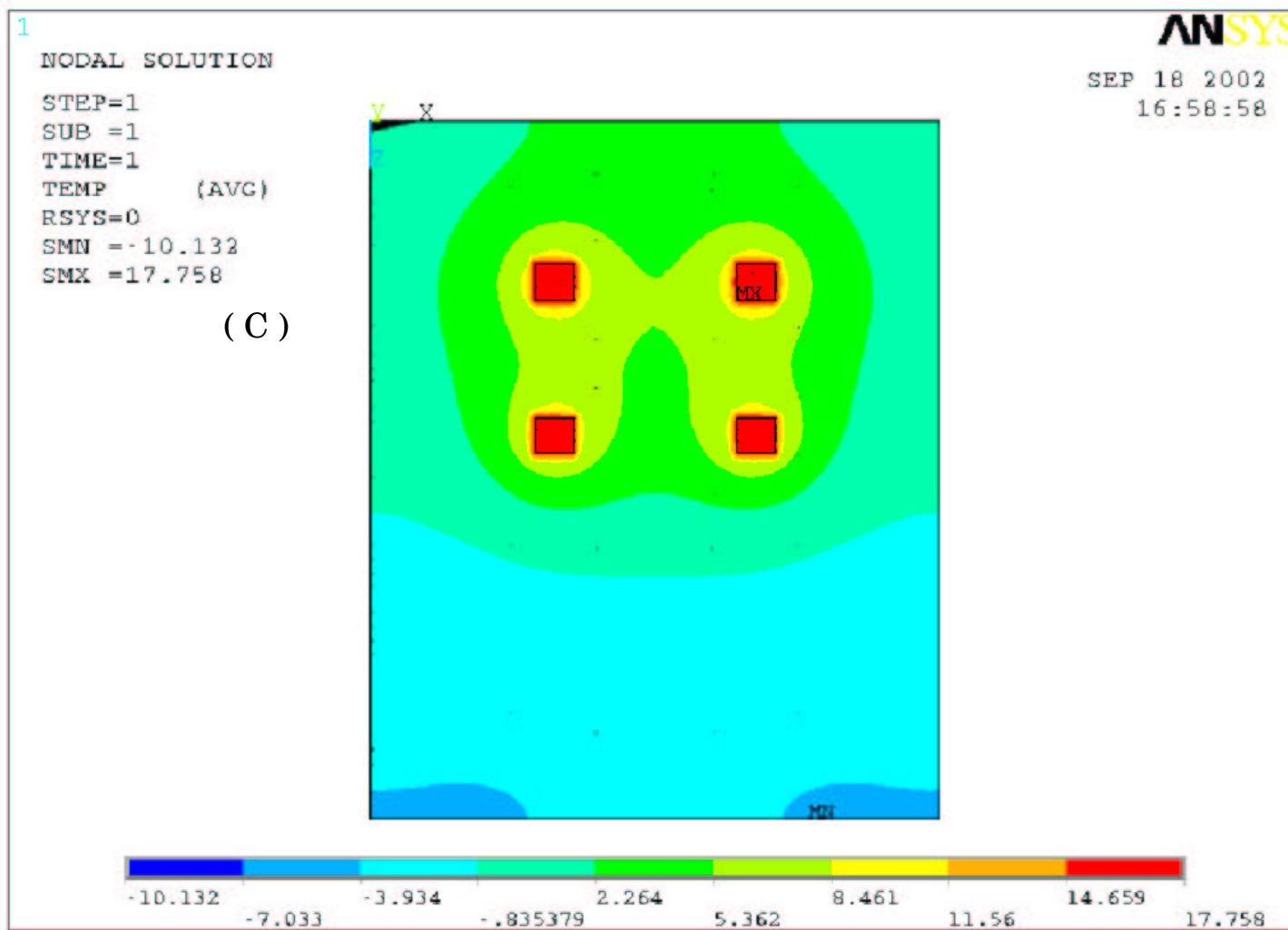


$a = 1.04 \text{ mm}$



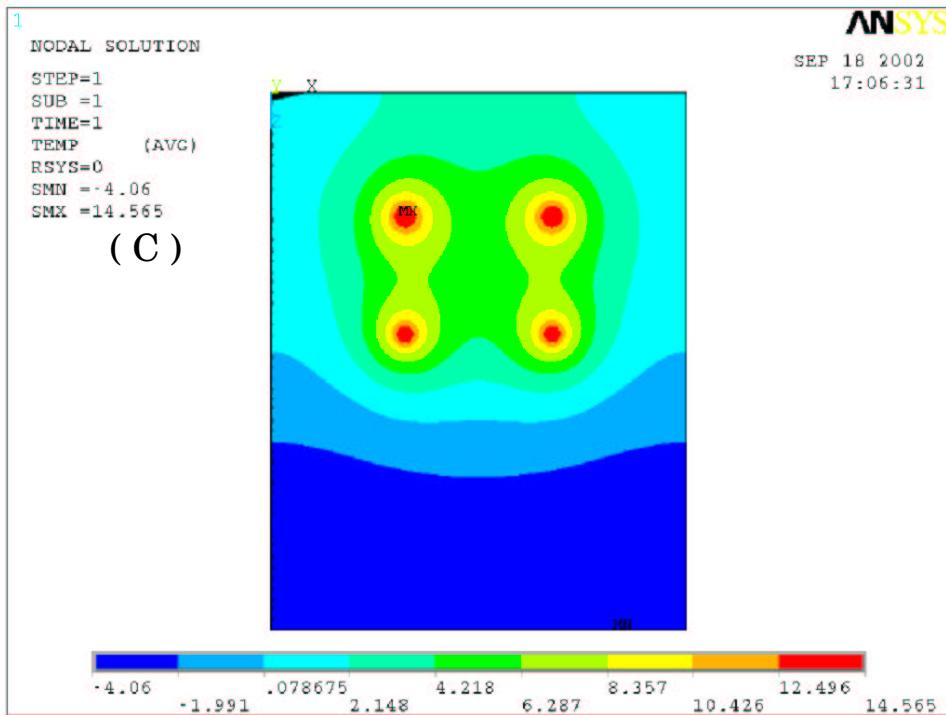
(W/m<sup>2</sup>)

# Thermal distribution ( for 6 vias MPC : a=0.65mm)

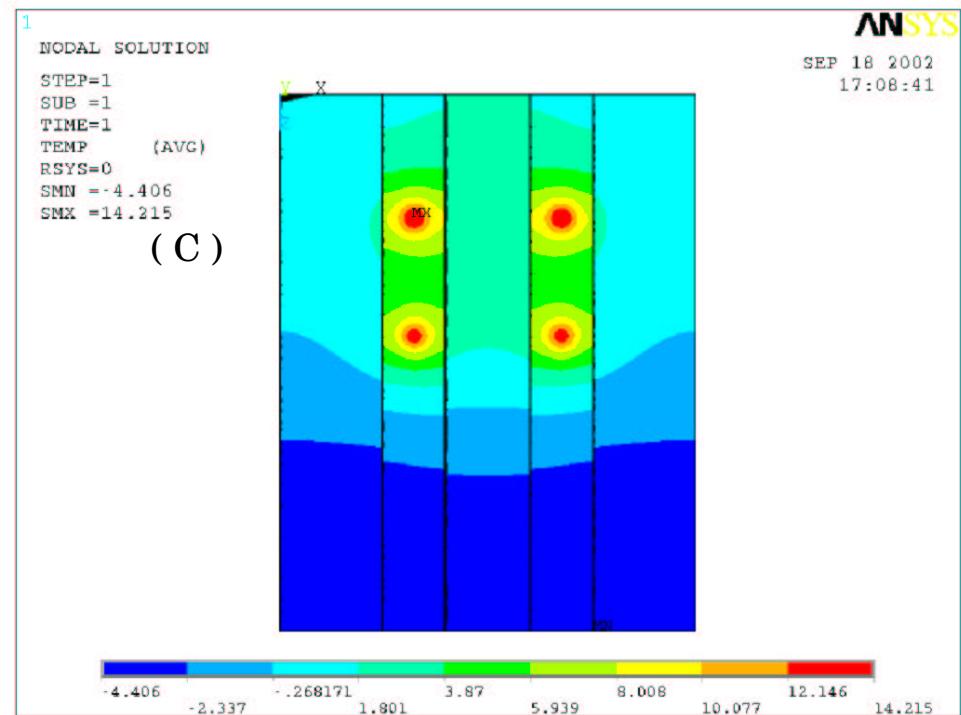


# Thermal distribution at the GNDs ( for 6 vias MPC : a=0.65 mm)

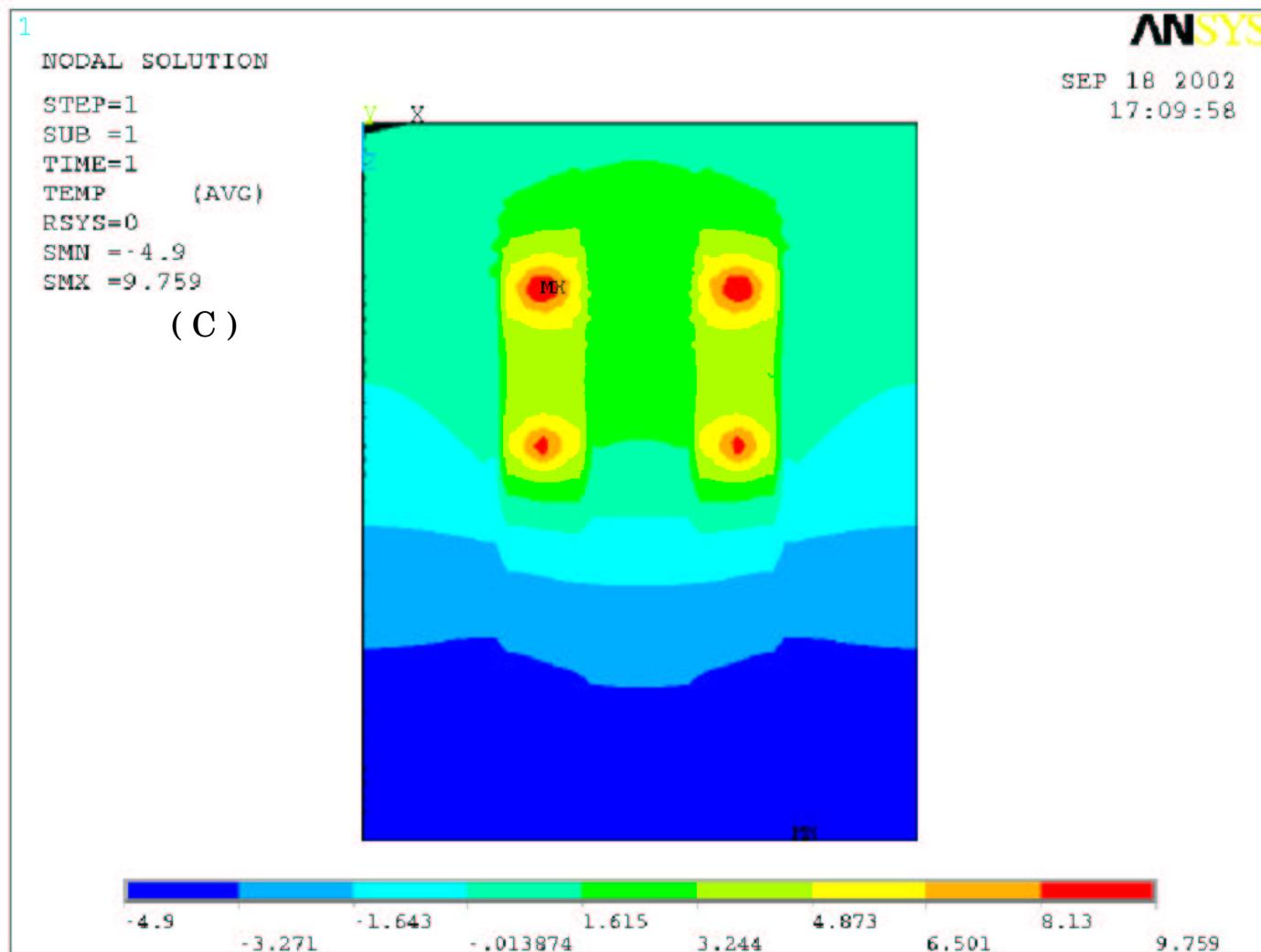
up-side



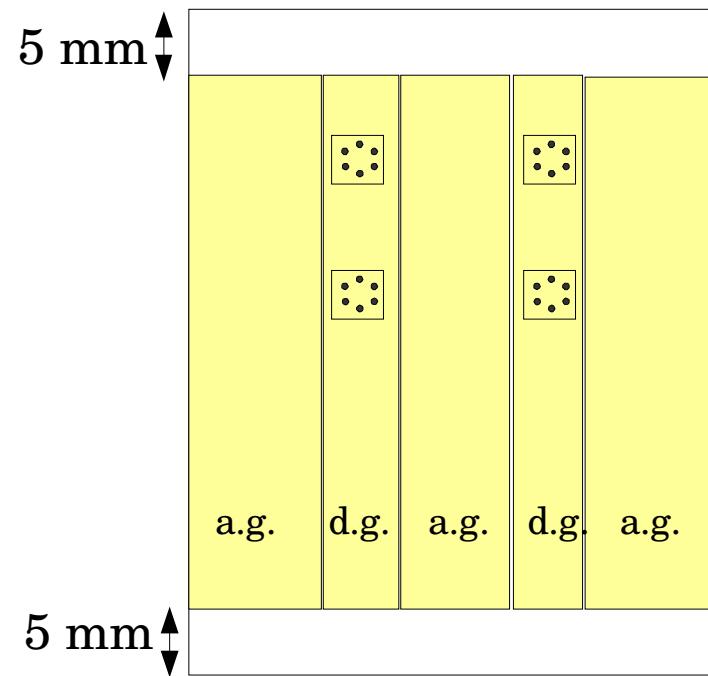
down-side



# Thermal distribution at the glue layer ( for 6 vias MPC : a=0.65 mm)

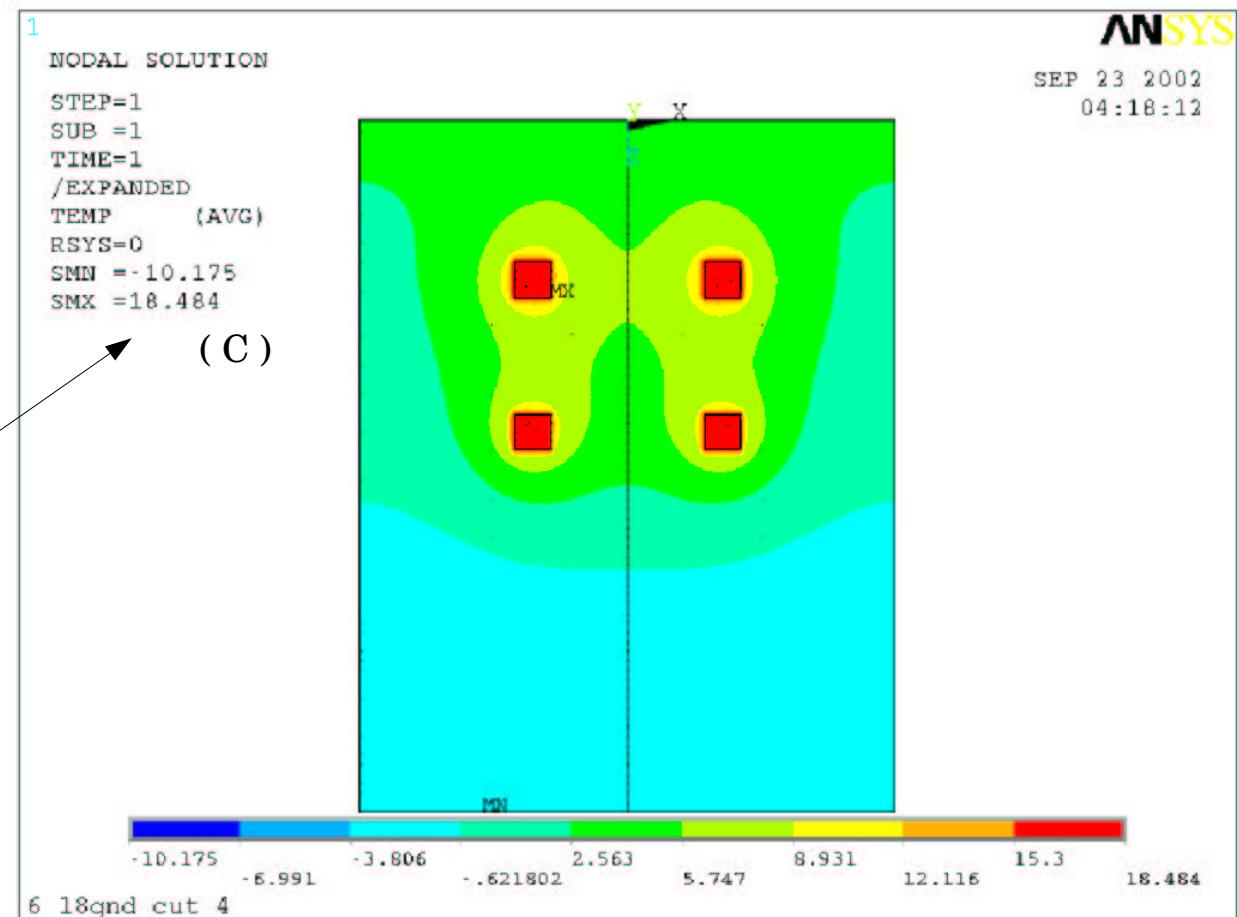


## Cutting the edge of the down-side GNDs (for 6 vias MPC : a=0.65 mm)



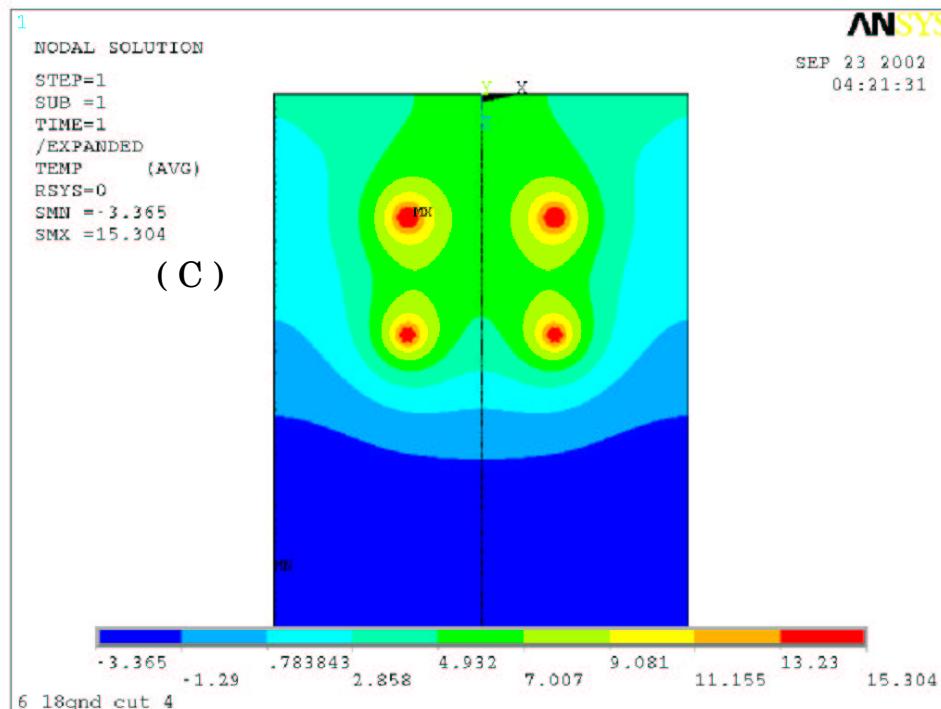
cut : ~18.48 C

cf) no-cut :~17.76 C

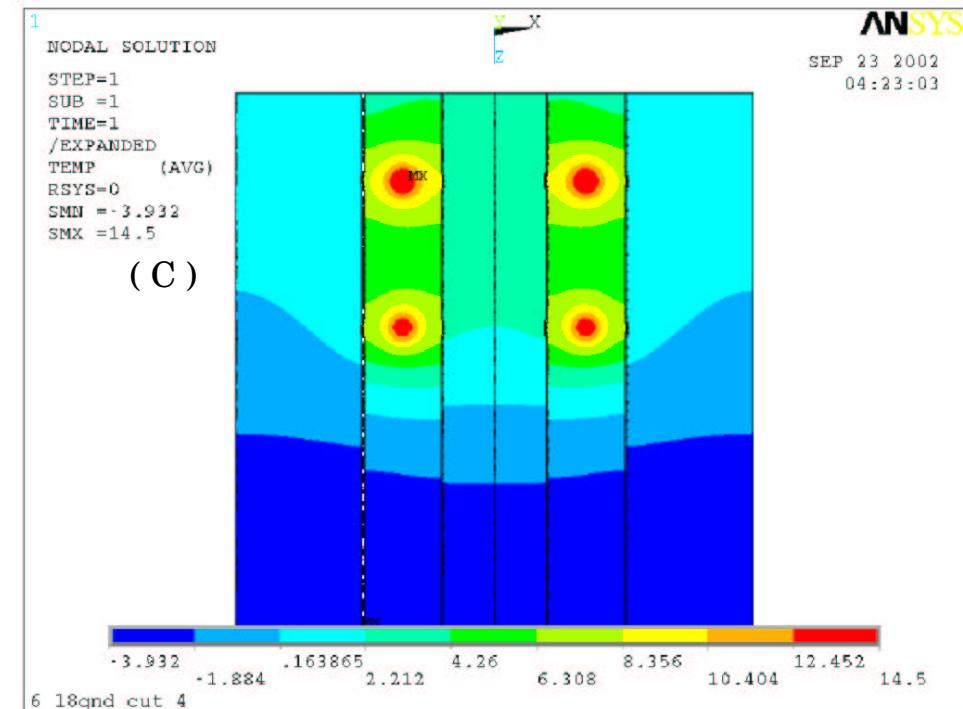


## Thermal distribution at the GNDs (for 6 vias MPC : a=0.65 mm)

up-side

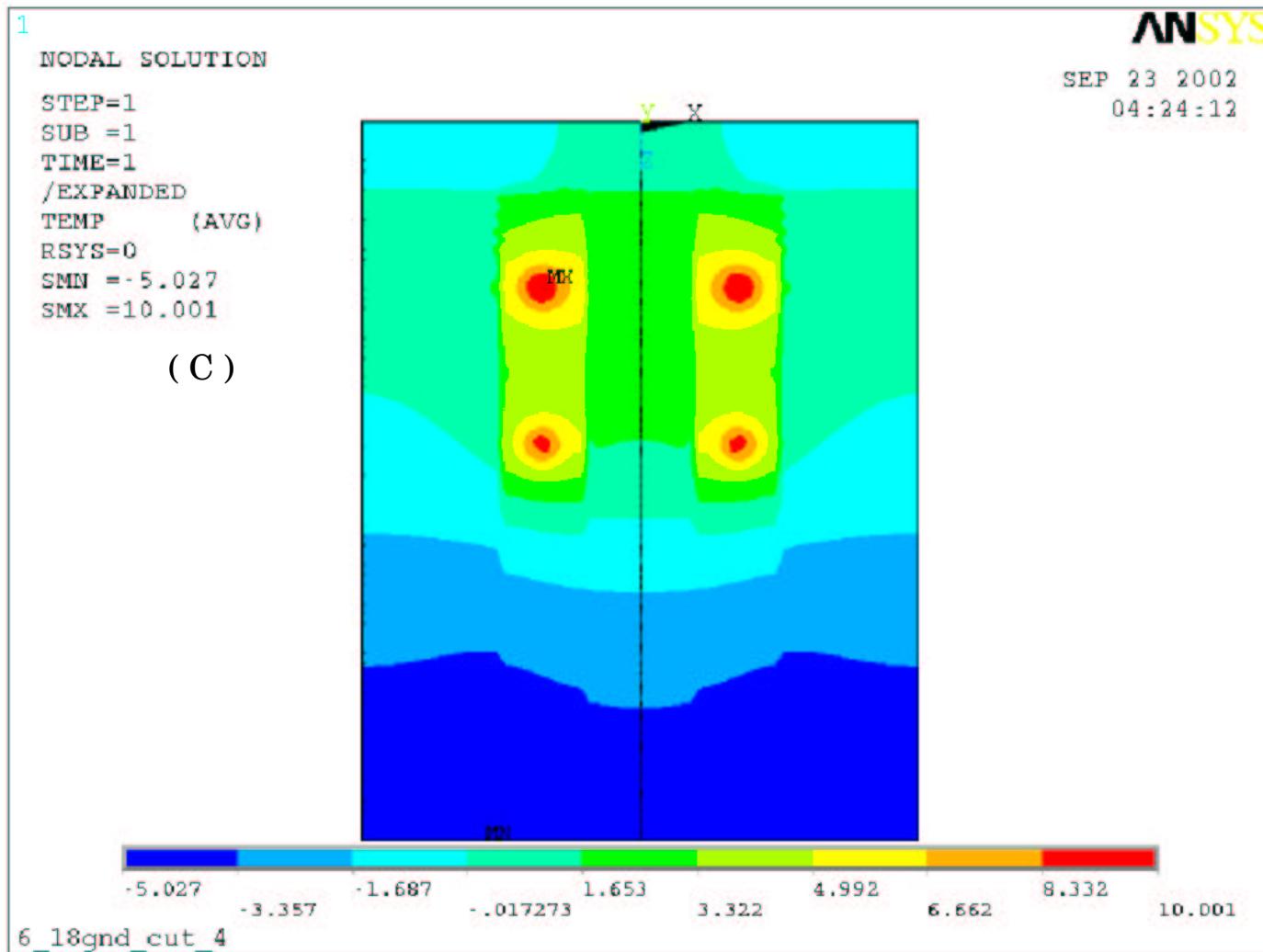


down-side

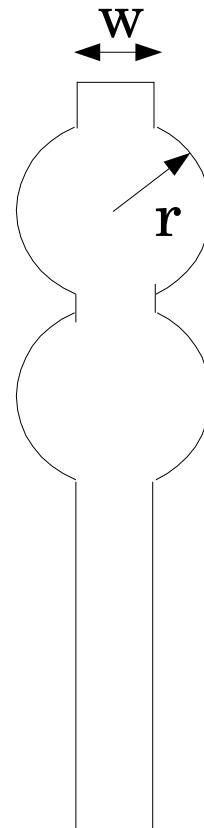
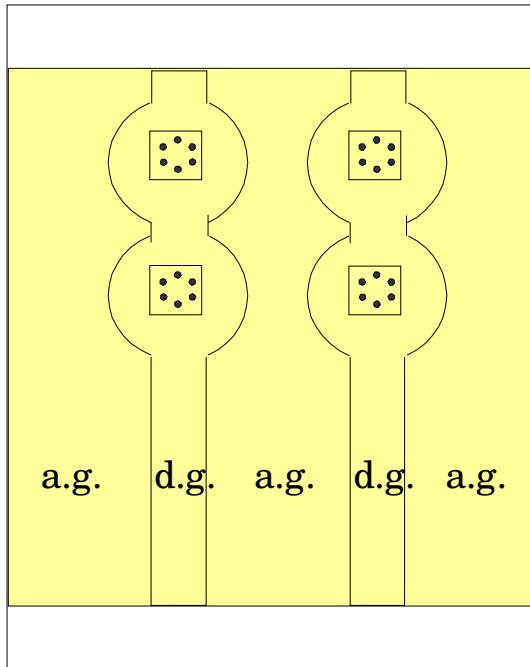


# Thermal distribution at the glue layer

(for 6 vias MPC :  $a=0.65$  mm )



## Modifying the "down-side" GNDs. (for 6 vias MPC : a=0.65 mm)



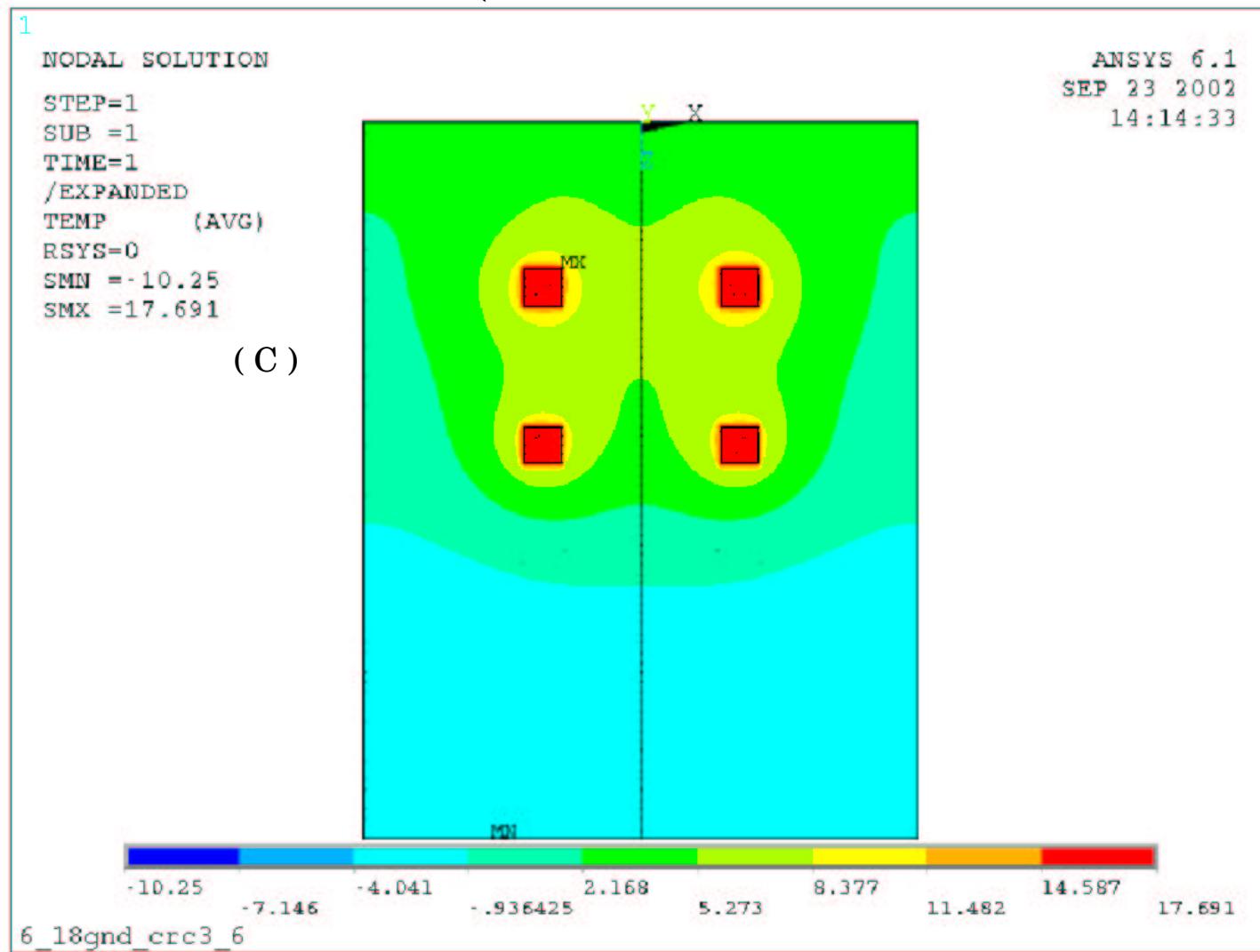
Maximum temperature ( C ) (mm)

$w \setminus r$	3	4	5	6
3	19.23	18.57	18.07	17.69
4	19.00	18.40	18.02	17.71
5	18.71	18.32	18.01	17.71
6	18.37	18.23	17.96	17.71

cf ) not modified : ~18.48 C

# Thermal distribution

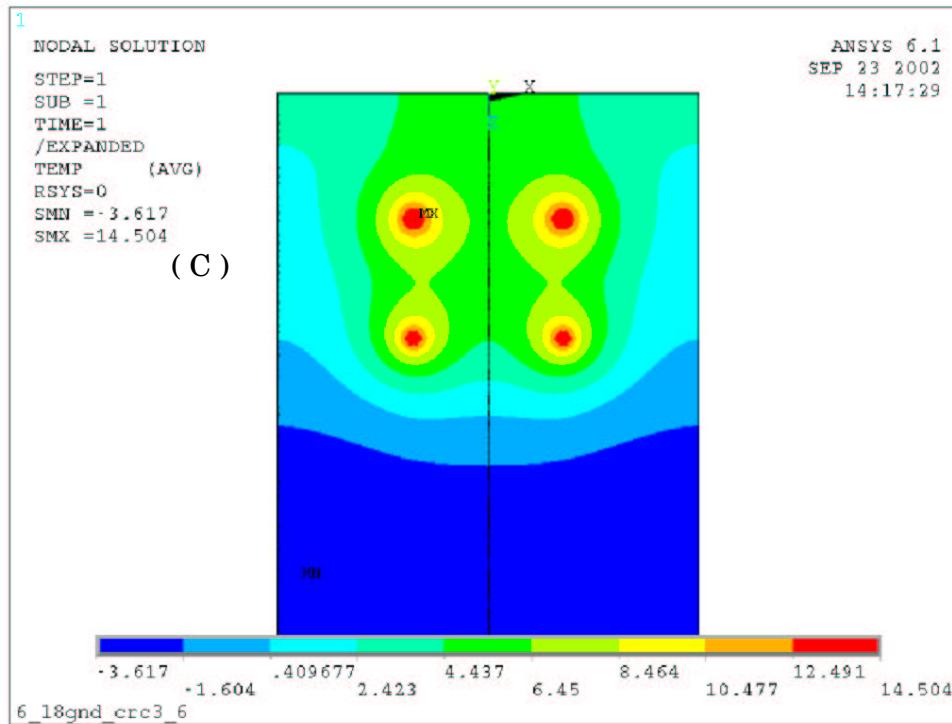
(for 6 vias MPC :  $a=0.65$  mm :  $w/r = 3\text{mm}/6\text{mm}$ )



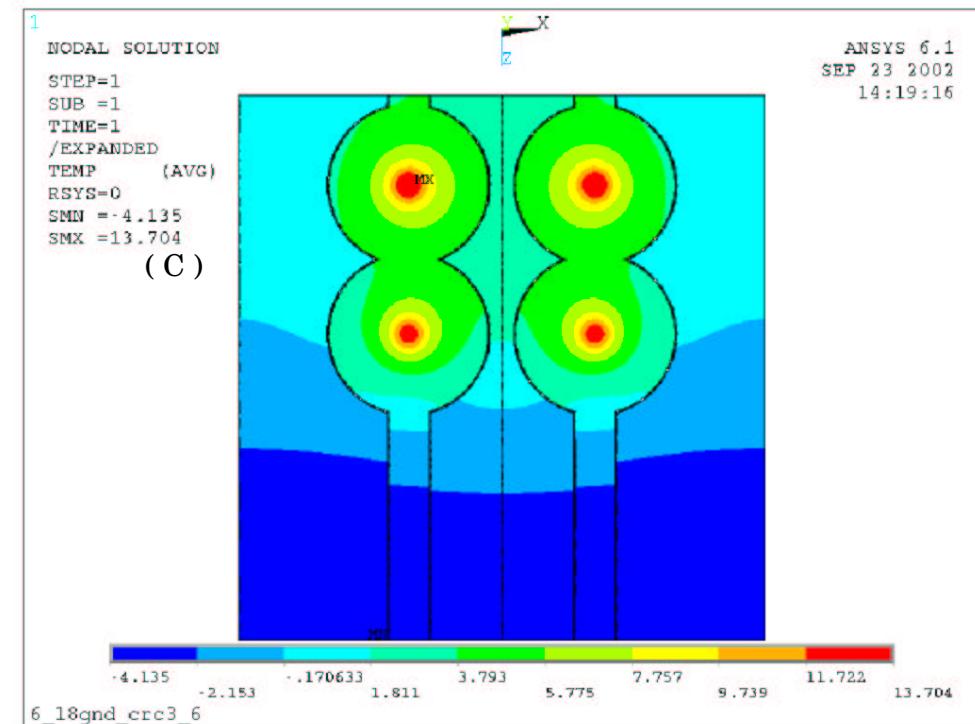
# Thermal distribution at the GND layers

(for 6 vias MPC :  $a=0.65$  mm :  $w/r = 3\text{mm}/6\text{mm}$ )

up-side

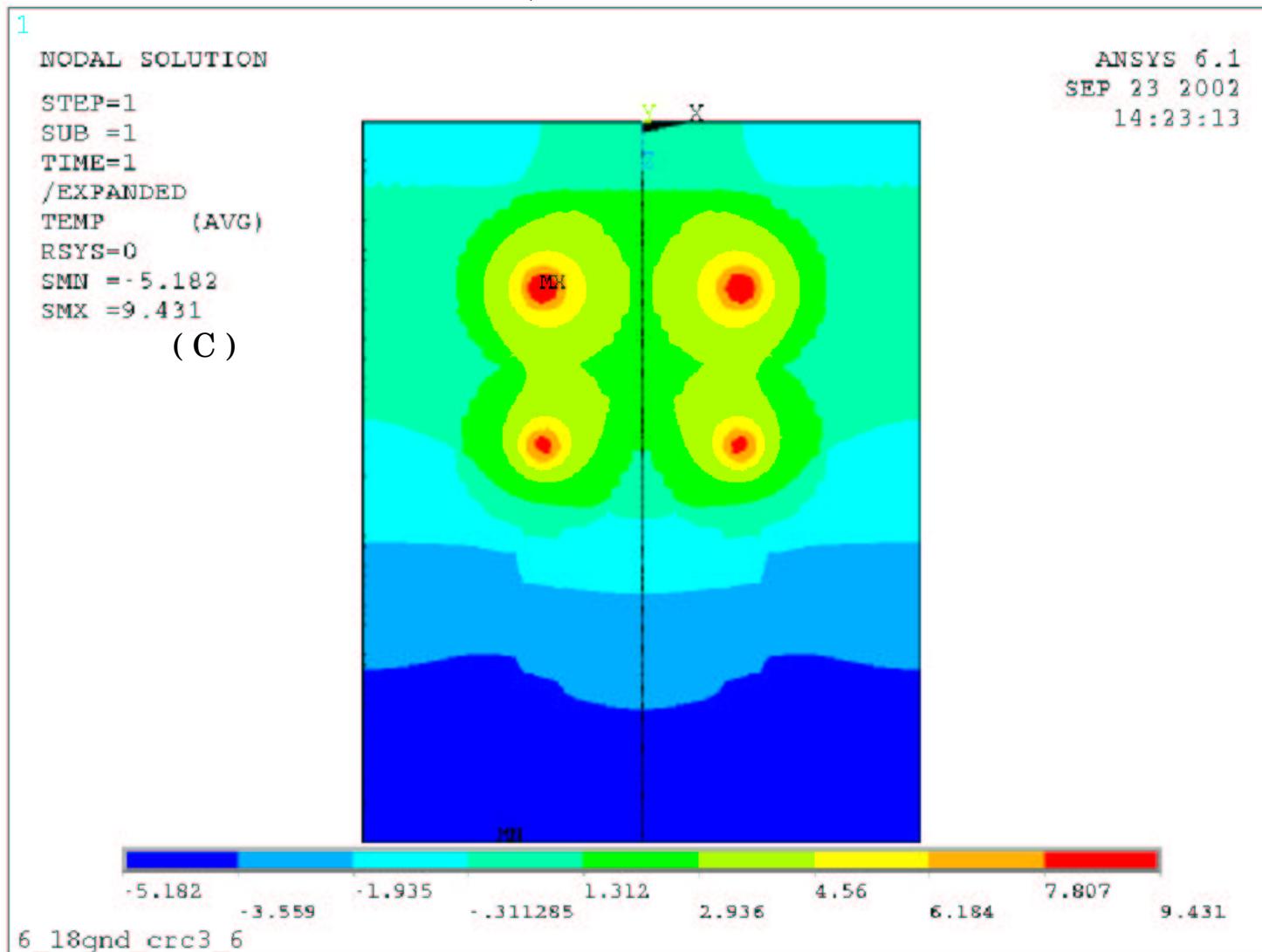


down-side



# Thermal distribution at the glue layer

(for 6 vias MPC :  $a=0.65$  mm :  $w/r = 3\text{mm}/6\text{mm}$ )

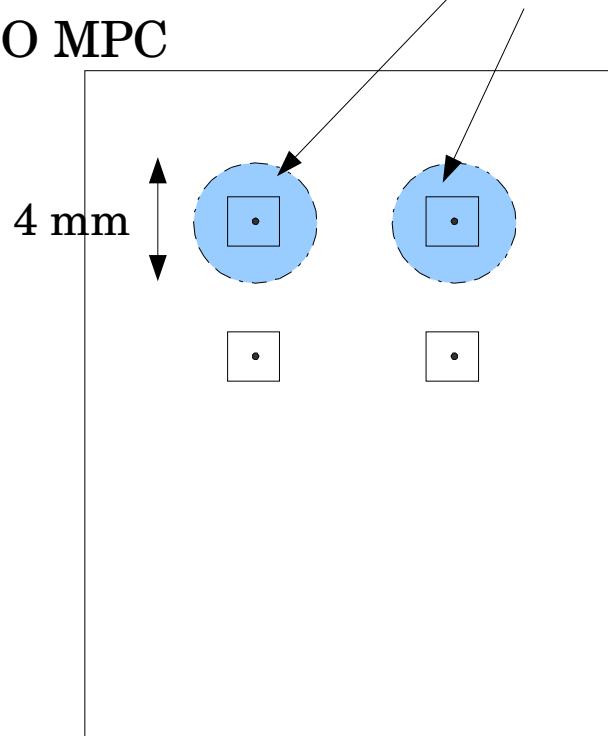


# Air in the glue

for BeO and polyimide MPC(w/r=3/6)

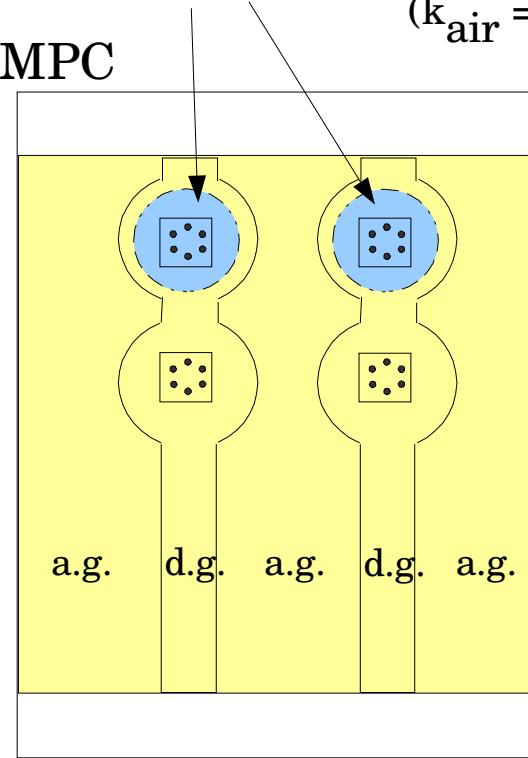
Remove the epoxy in this area and replace it with air

BeO MPC



Polyimide MPC

( $k_{air} = 0.023 \text{ W/m-K}$ )



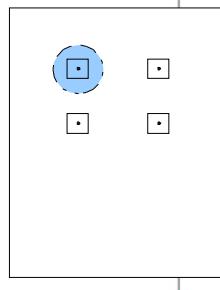
Result

	BeO	Polyimide
normal	5.66	17.69
1 hole	5.94	19.71
2 holes	5.99	19.87

( C )

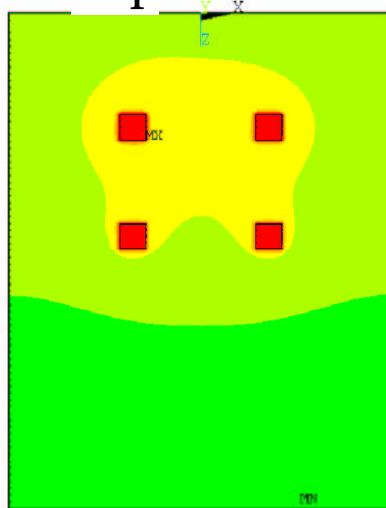
1  
NODAL SOLUTION  
TIME=1  
TEMP (AVG)  
RSYS=0  
SMN = -9.335  
SMX = 5.936

BeO MPC  
1 hole



Top

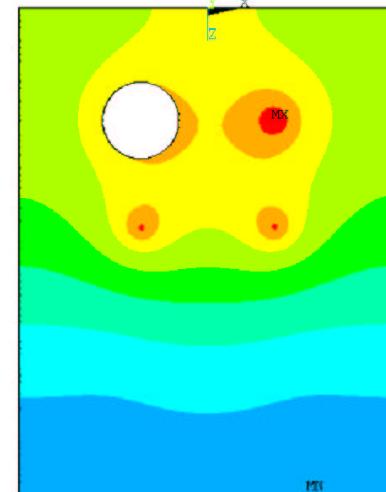
ANSYS 6.1  
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15:07:54



-9.335 -7.638 -5.942 -4.245 -2.548 -.850979 .845883 2.543 4.24 5.936

Glue

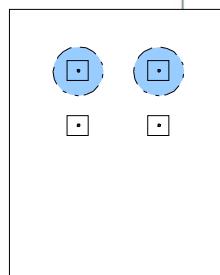
ANSYS 6.1  
SEP 26 2002  
15:08:58



AL SOLUTION  
P=1  
E=1  
IE=1  
IP (AVG)  
S=0  
I = -3.183  
I = 2.484

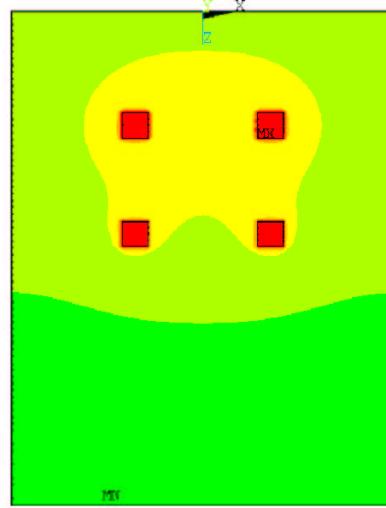
1  
NODAL SOLUTION  
STEP=1  
SUB =1  
TIME=1  
TEMP (AVG)  
RSYS=0  
SMN = -9.321  
SMX = 5.987

2 holes



Top

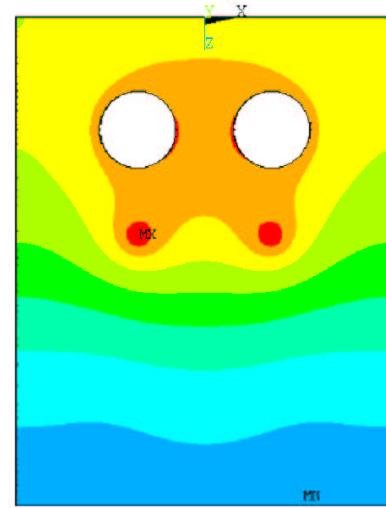
ANSYS 6.1  
SEP 26 2002  
15:03:02



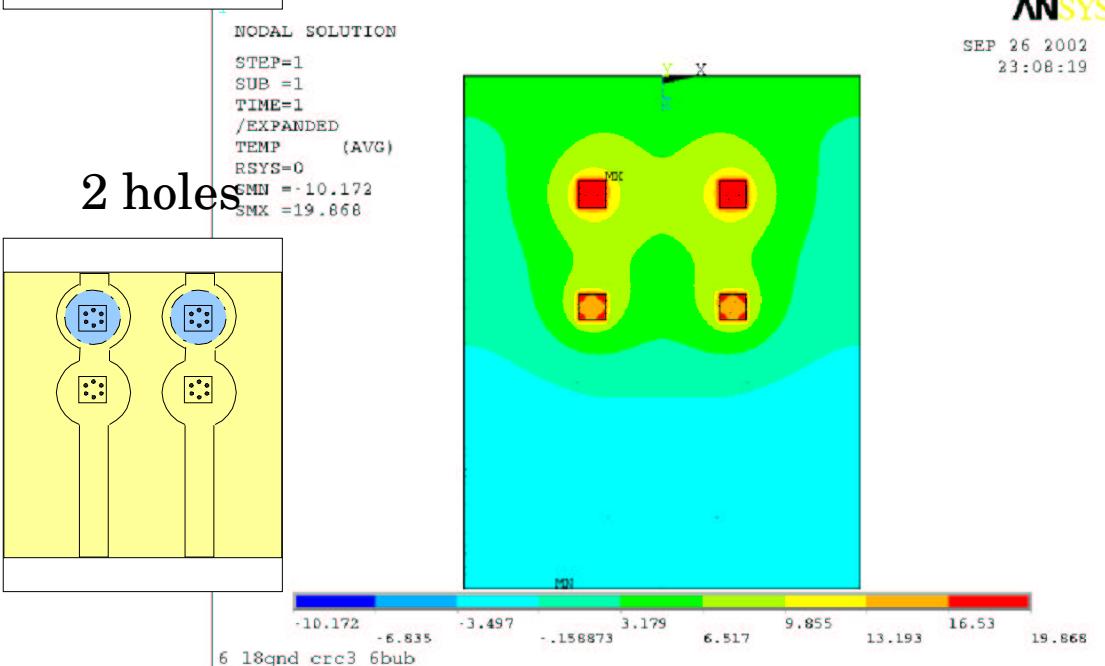
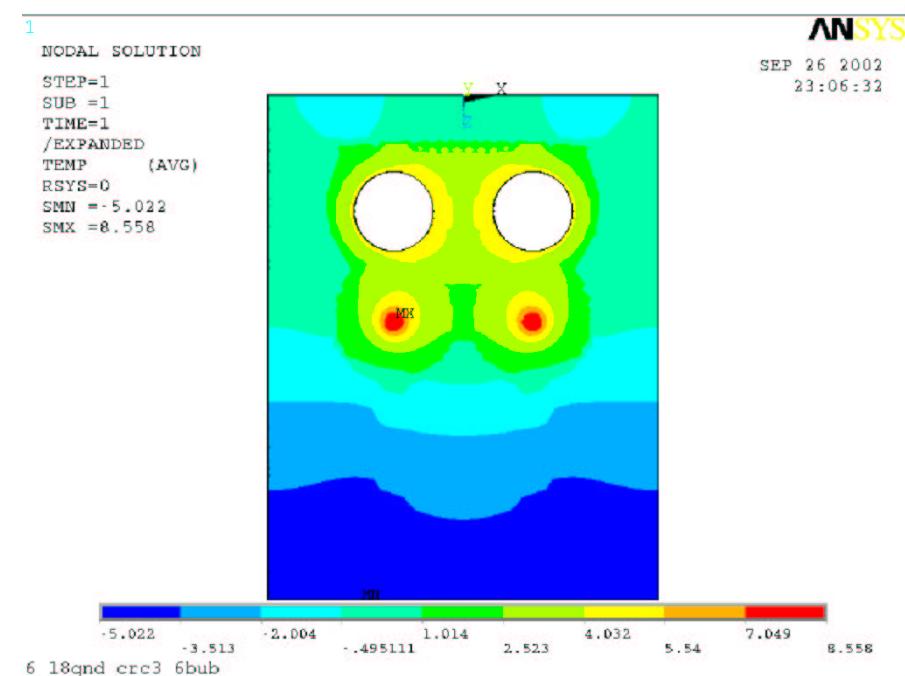
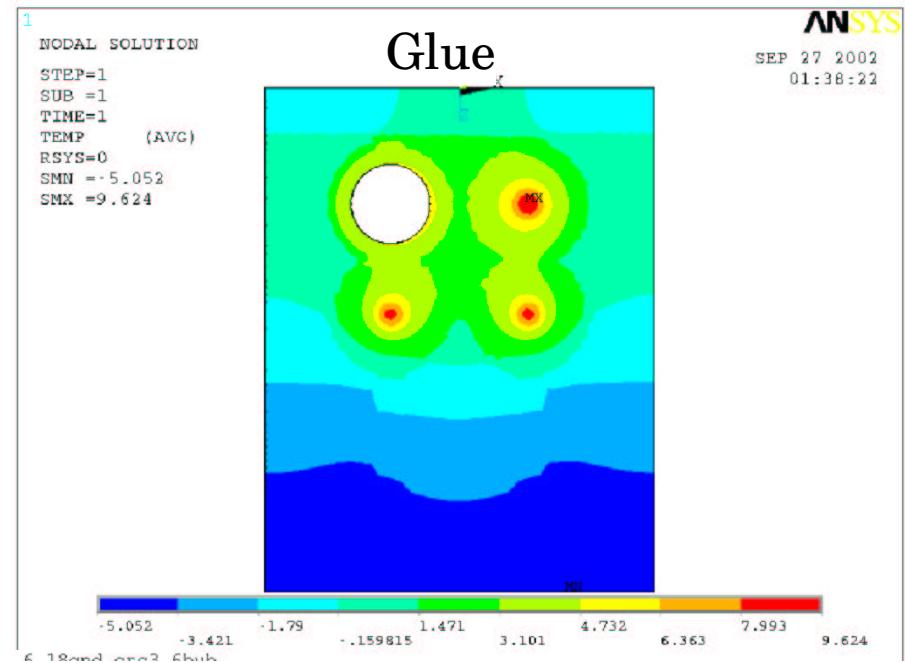
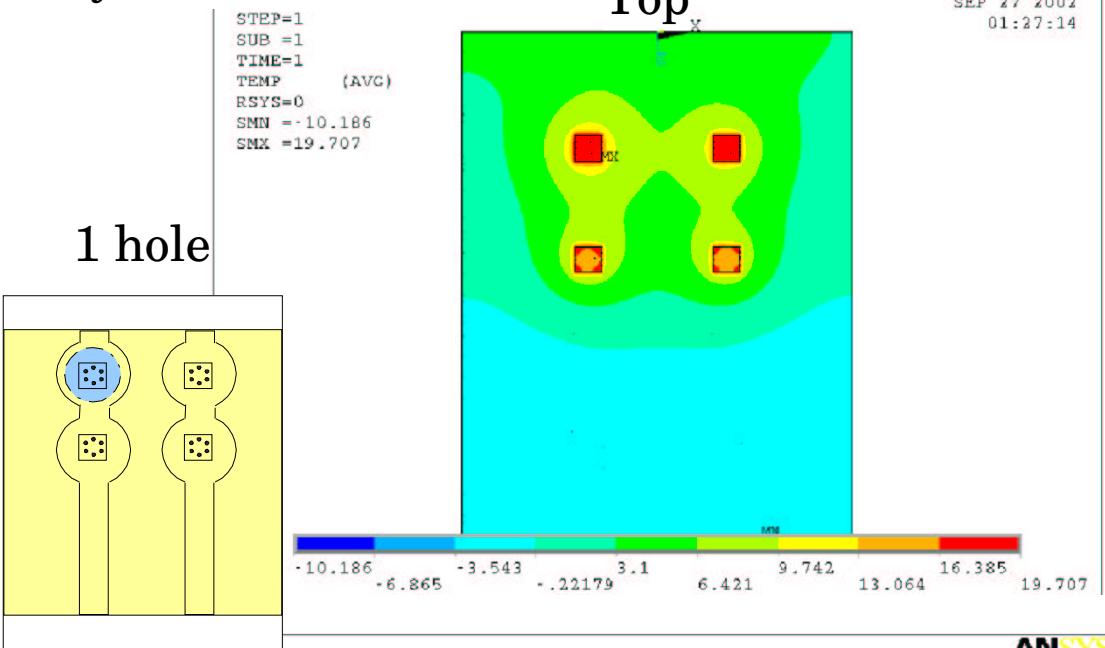
-9.321 -7.621 -5.92 -4.219 -2.518 -.816718 .884229 2.585 4.286 5.987

AL SOLUTION  
P=1  
E=1  
IE=1  
IP (AVG)  
S=0  
I = -3.155  
I = 2.008

ANSYS 6.1  
SEP 26 2002  
15:05:33

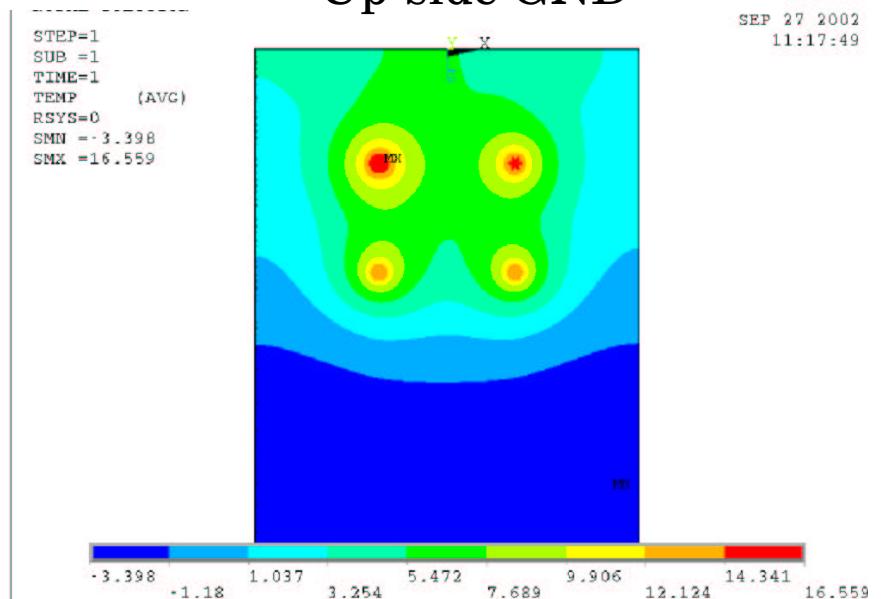


# Polyimide MPC

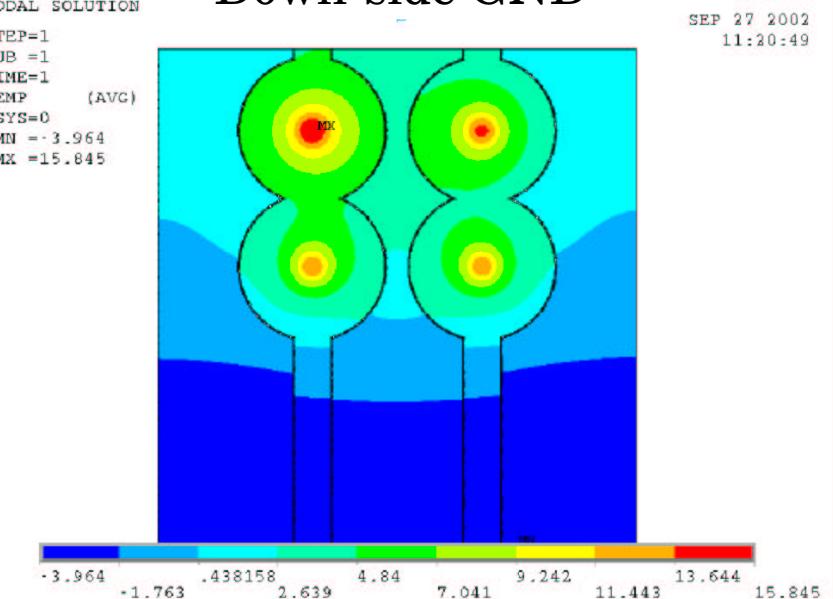


# Polyimide MPC

1 hole



# Down-side GND



2 holes

